

**N-Channel Enhancement Mode Power MOSFET**

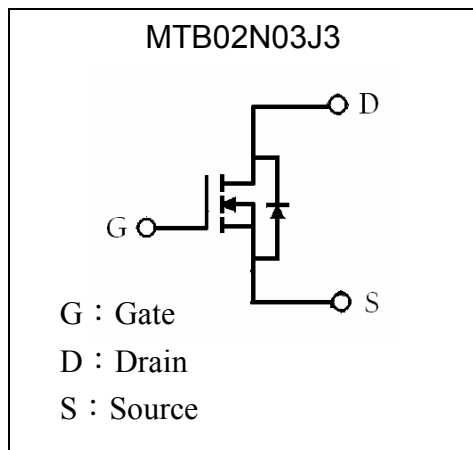
# MTB02N03J3

<b>BV<sub>DSS</sub></b>	<b>30V</b>
<b>I<sub>D</sub></b>	<b>136A</b>
<b>R<sub>DS(ON)</sub>@ V<sub>GS</sub>=10V, I<sub>D</sub>=45A</b>	<b>2.7mΩ (typ)</b>
<b>R<sub>DS(ON)</sub>@ V<sub>GS</sub>=4.5V, I<sub>D</sub>=36A</b>	<b>3.8mΩ (typ)</b>

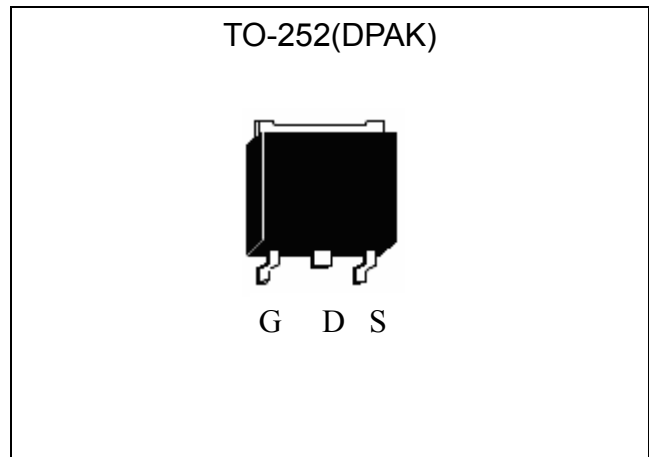
**Features**

- Simple Drive Requirement
- Repetitive Avalanche Rated
- Fast Switching Characteristic
- RoHS compliant package & Halogen-free package

**Symbol**

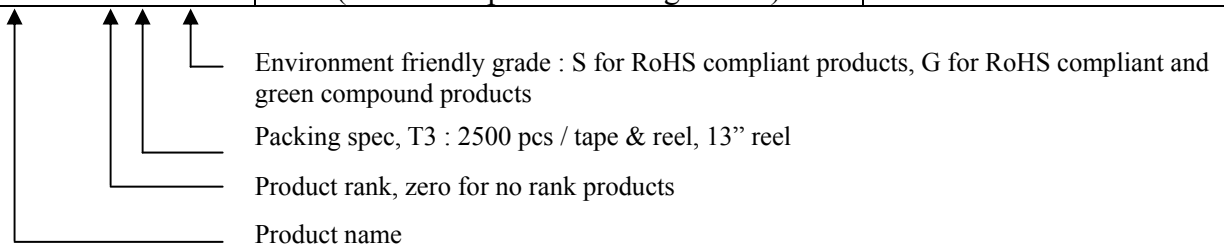


**Outline**



**Ordering Information**

Device	Package	Shipping
MTB02N03J3-0-T3-G	TO-252 (RoHS compliant & Halogen-free)	2500 pcs / Tape & Reel





**Absolute Maximum Ratings** (Tc=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	
Continuous Drain Current @ Tc=25°C	I <sub>D</sub>	136	A
Continuous Drain Current @ Tc=100°C		86	
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	300	
Avalanche Current	I <sub>AS</sub>	60	
Avalanche Energy @ L=0.3mH, I <sub>D</sub> =60A, R <sub>G</sub> =25 Ω	E <sub>AS</sub>	540	mJ
Repetitive Avalanche Energy @ L=0.05mH (Note 2)	E <sub>AR</sub>	9	
Total Power Dissipation @ Tc=25°C	P <sub>d</sub>	93	W
Total Power Dissipation @ Tc=100°C		37	
Operating Junction and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55~+150	°C

Note : 1. Pulse width limited by maximum junction temperature  
 2. Duty cycle ≤ 1%

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>th,j-c</sub>	1.35	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>th,j-a</sub>	50 (Note)	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>th,j-a</sub>	110	°C/W

Note : When mounted on the minimum pad size recommended (PCB mount).

**Characteristics (Tc=25°C, unless otherwise specified)**

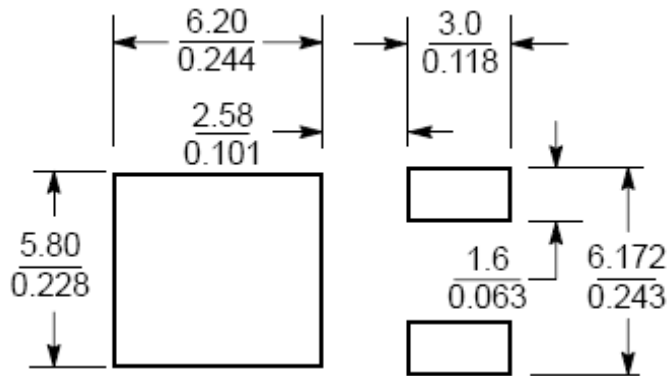
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1	1.6	2.5	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V
	-	-	25		V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>j</sub> =125°C
*R <sub>DS(ON)</sub>	-	2.7	3	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =45A
	-	3.8	6		V <sub>GS</sub> =4.5V, I <sub>D</sub> =36A
*G <sub>FS</sub>	-	17	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =45A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	108	-	nC	I <sub>D</sub> =45A, V <sub>DS</sub> =15V, V <sub>GS</sub> =10V
*Q <sub>gs</sub>	-	31	-		
*Q <sub>gd</sub>	-	43	-		
*t <sub>d(ON)</sub>	-	72	-	ns	V <sub>DS</sub> =15V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>GS</sub> =6 Ω, R <sub>L</sub> =15 Ω
*t <sub>r</sub>	-	82	-		
*t <sub>d(OFF)</sub>	-	172	-		
*t <sub>f</sub>	-	71	-		



Ciss	-	7032	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz
Coss	-	898	-		
Crss	-	843	-		
Rg	-	2	-	Ω	V <sub>GS</sub> =15mV, V <sub>DS</sub> =0V, f=1MHz
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	45	A	
*I <sub>SM</sub>	-	-	180		
*V <sub>SD</sub>	-	0.8	1.3	V	I <sub>F</sub> =45A, V <sub>GS</sub> =0V
*trr	-	50	-	ns	I <sub>F</sub> =45A, V <sub>GS</sub> =0, dI <sub>F</sub> /dt=100A/μs
*Qrr	-	35	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle ≤2%

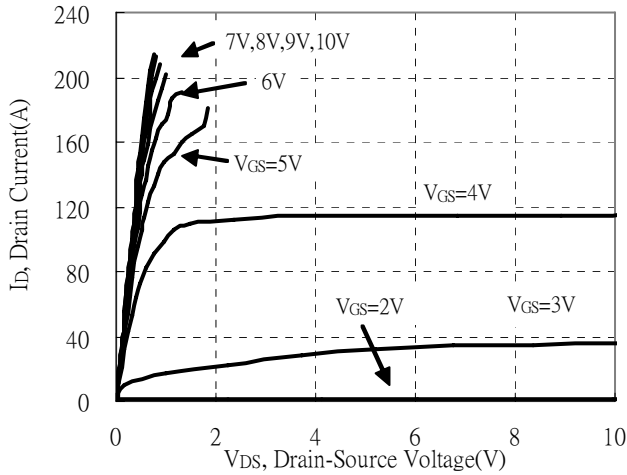
**Recommended soldering footprint**



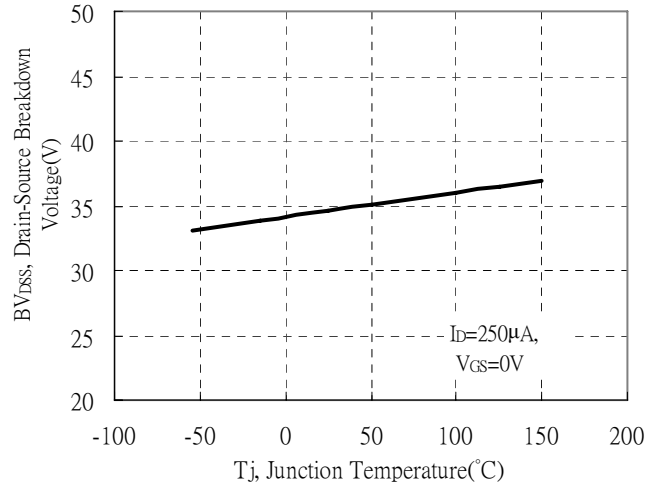
Unit (  $\frac{\text{mm}}{\text{inch}}$  )

**Typical Characteristics**

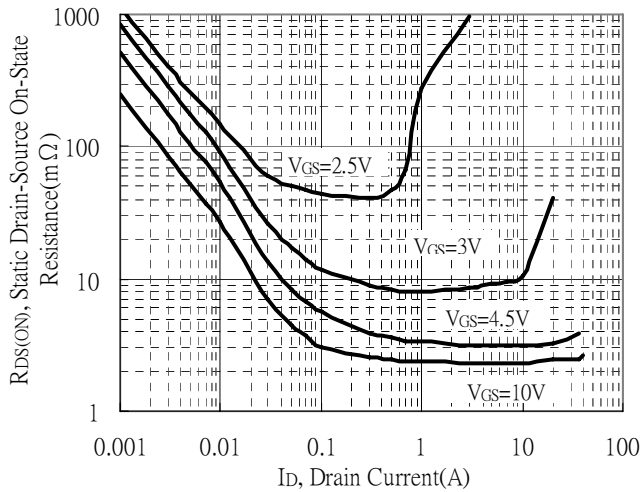
Typical Output Characteristics



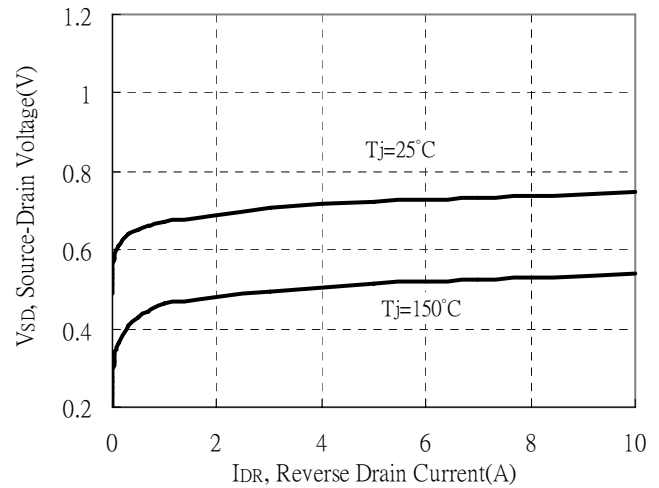
Brekdown Voltage vs Ambient Temperature



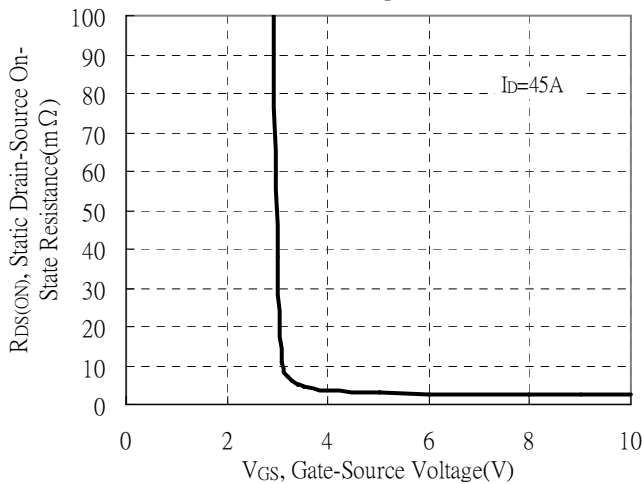
Static Drain-Source On-State resistance vs Drain Current



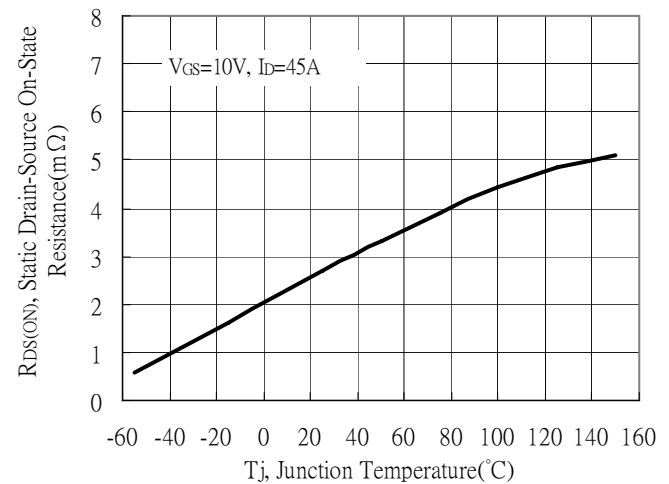
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

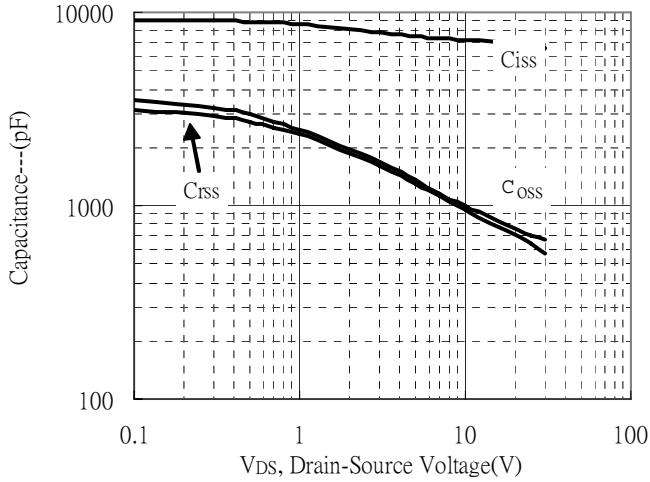


Drain-Source On-State Resistance vs Junction Temperature

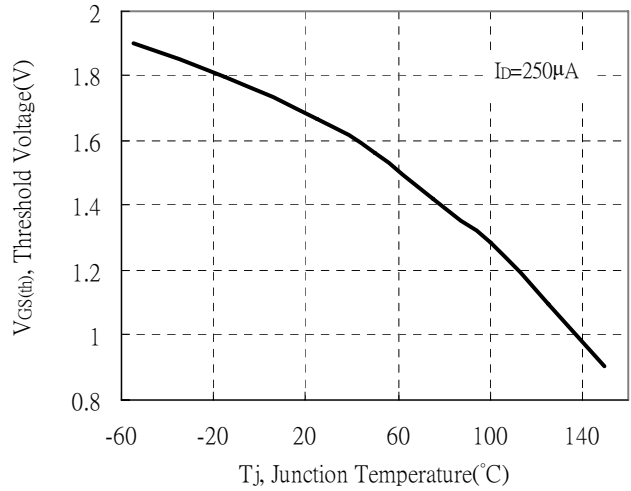


**Typical Characteristics(Cont.)**

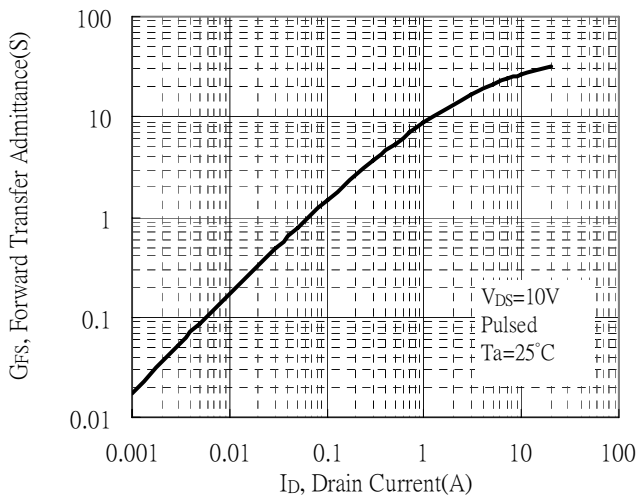
Capacitance vs Drain-to-Source Voltage



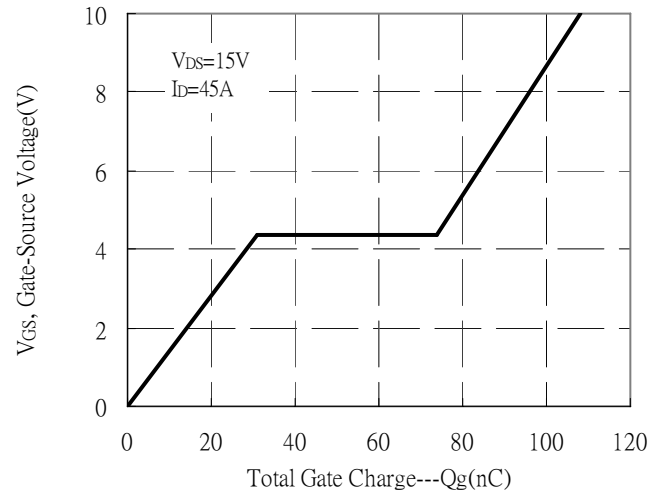
Threshold Voltage vs Junction Temperature



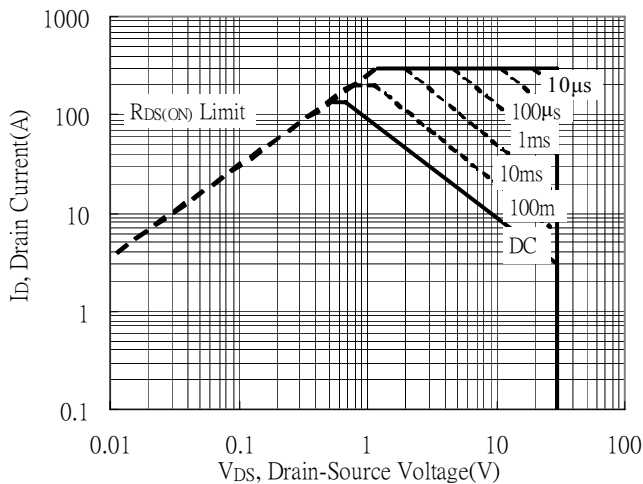
Forward Transfer Admittance vs Drain Current



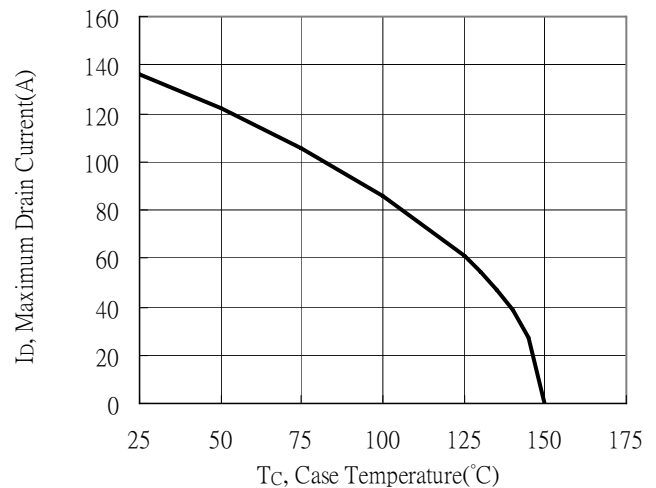
Gate Charge Characteristics



Maximum Safe Operating Area

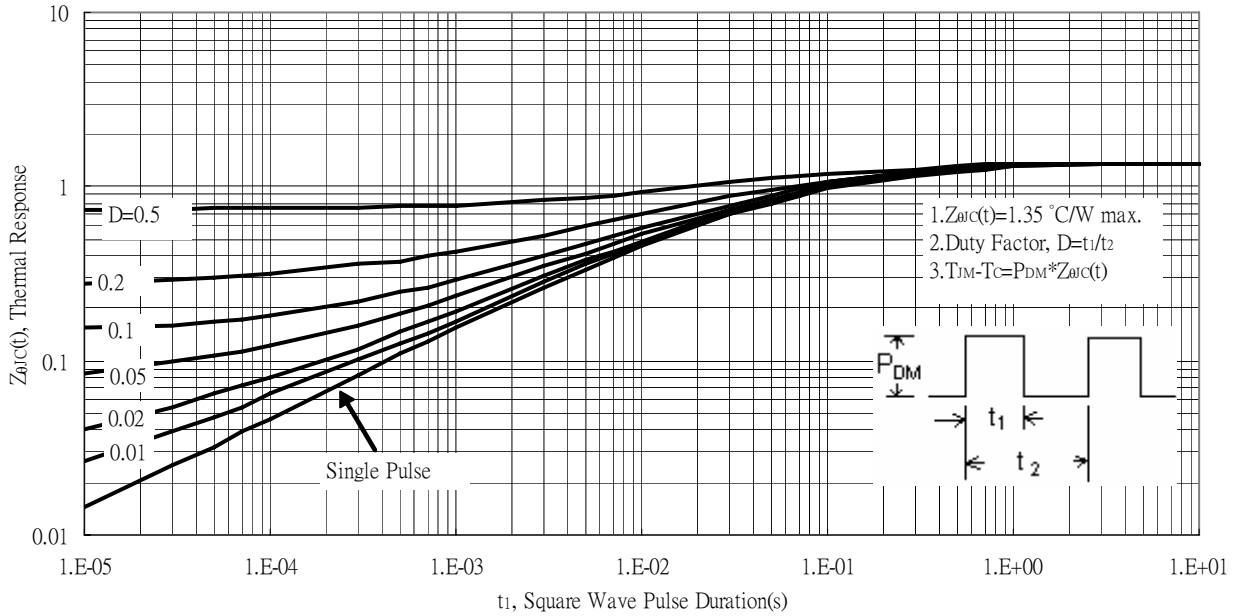


Maximum Drain Current vs Case Temperature



**Typical Characteristics(Cont.)**

Transient Thermal Response Curves

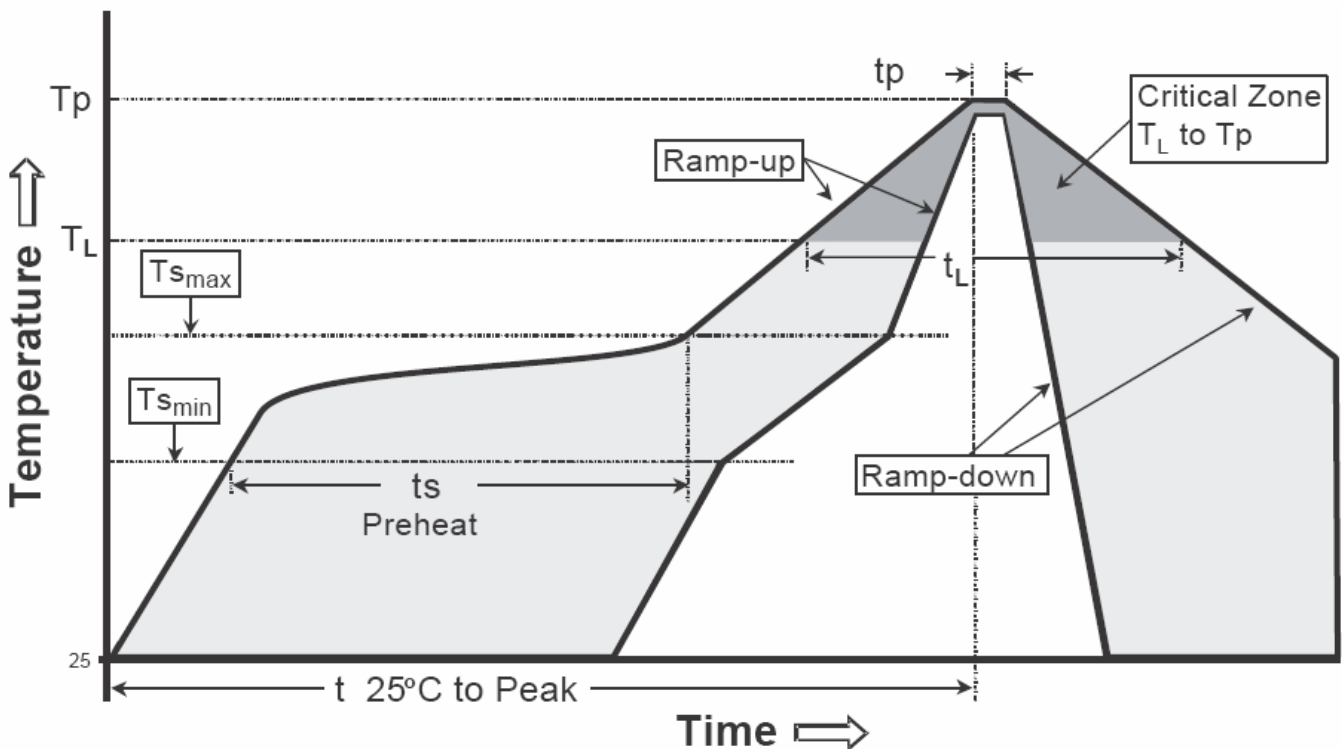




**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**

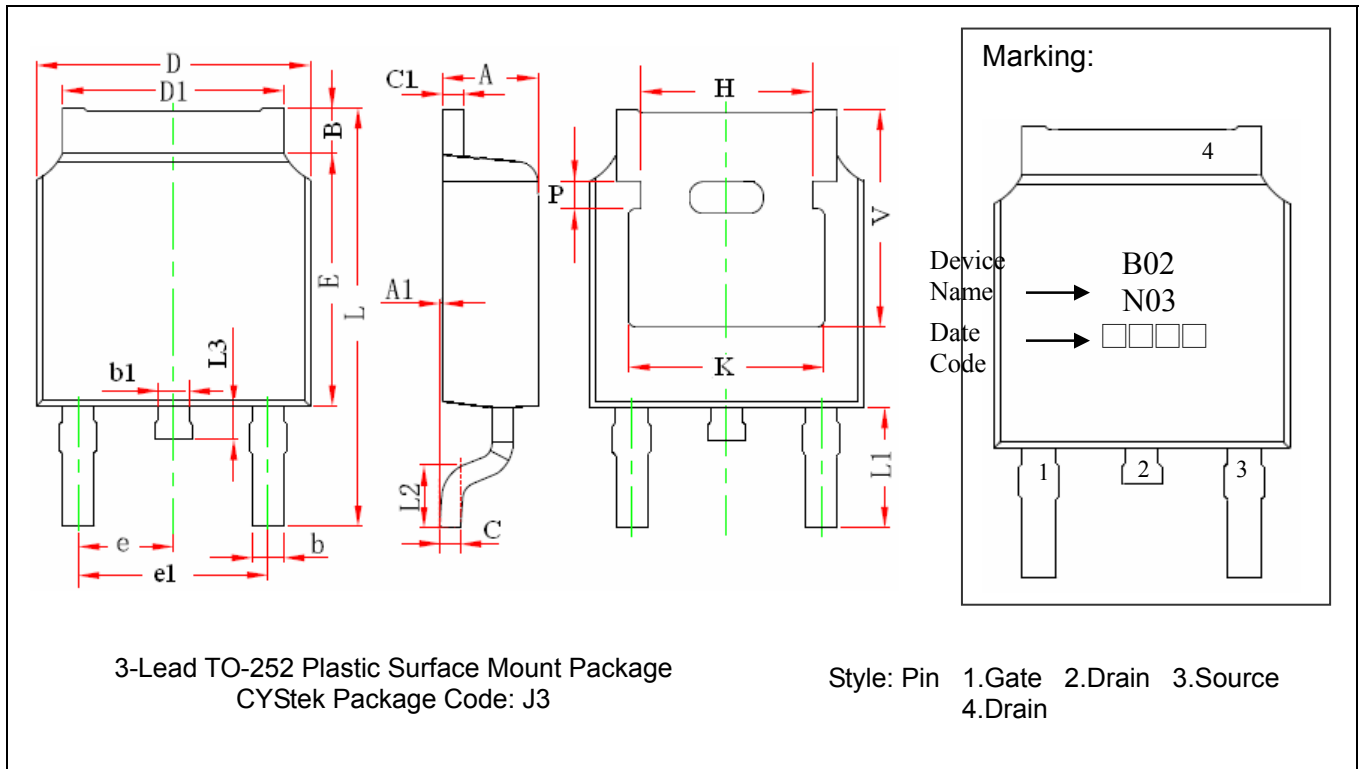


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.



**TO-252 Dimension**



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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