

Continental Device India Limited

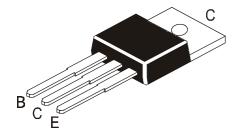
An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company



PNP SILICON EPITAXIAL POWER DARLINGTON TRANSISTOR

CSB1626

TO-220 Plastic Package



Complementary CSD2495

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

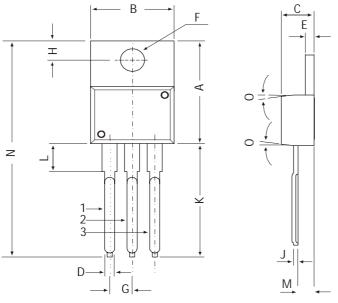
DESCRIPTION	SYMBOL	VALUE	UNIT
Collector -Base Voltage(open emitter)	V_{CBO}	110	V
Collector -Emitter Voltage(open base)	V_{CEO}	110	V
Emitter Base Voltage(open collector)	V_{EBO}	5.0	V
Collector Current	I_{C}	6.0	Α
Base Current	I_{B}	1.0	Α
Total Power Dissipation upto Tc=25°C	P_{tot}	30	W
Junction Temperature	T _i	150	°C
Storage Temperature	$T_{sta}^{'}$	-65 to +150	°C

ELECTRICAL CHARACTERISTICS (Tc=25°C Unless Otherwise Specified)

DESCRIPTION		SYMBOL TEST CONDITION		MIN	TYP	MAX	UNIT
							_
Collector Cut off Current		I _{CBO}	$V_{CB}=110V$, $I_{E}=0$			100	μΑ
Emitter Cut off Current		I_{EBO}	$V_{EB}=5V, I_{C}=0$			2	mΑ
Breakdown Voltages		V_{CEO}	$I_C=30$ mA, $I_B=0$	110			V
Collector Emitter Saturation Voltage		$V_{CE(sat)}$	$I_C=5A$, $I_B=5mA$			2.5	V
_		$V_{BE(sat)}$	$I_C=5A$, $I_B=5mA$			3	V
DC Current Gain		h _{FE}	$I_{C}=5A, V_{CE}=4V$	5		30	K
	O/P	h _{FE}		5		20	K
Transistors frequency		f _T	I_{C} =0.5A, V_{CE} =12V		60		MHz
Output Capacitance		C_ob	$V_{CB}=10V,I_{E}=0,f=1MHz$		55		pF

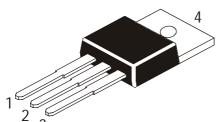
TO-220 Plastic Package

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DIM	MIN	MAX		
Α	14.42	16.51		
В	9.63	10.67		
С	3.56	4.83		
D	_	0.90		
E	1.15	1.40		
F	3.75	3.88		
G	2.29	2.79		
Н	2.54	3.43		
J	_	0.56		
K	12.70	14.73		
L	2.80	4.07		
М	2.03	2.92		
N	_	31.24		
0	7 DEG			

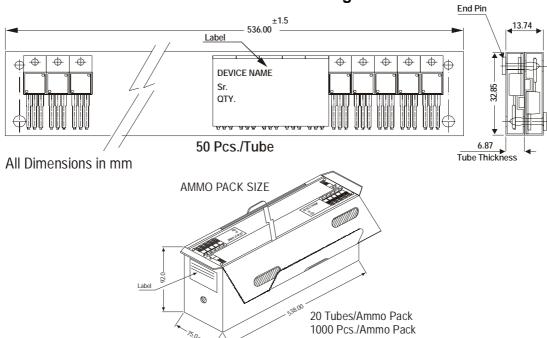
All diminsions in mm.



Pin Configuration

- 1. Base
- 2. Collector
- 3. Emitter
- 4. Collector

TO-220 Tube Packing



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-220 / FP	200 pcs/polybag 50 pcs/tube		3" x 7.5" x 7.5" 3.5" x 3.7" x 21.5"	1.0K 1.0K	17" x 15" x 13.5" 19" x 19" x 19"	16.0K 10.0K	36 kgs 29 kgs

Notes CSB1626

TO-220 Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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