

## 650V N-Channel MOSFET

## **Description**

The MSF12N65 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The TO-220F package is universally preferred for all commercial-industrial applications

#### **Features**

- Low gate charge (typical 52nC)
- High ruggedness
- · Fast switching
- · 100% avalanche tested
- Improved dv/dt capability
- · RoHS compliant package

### **Application**

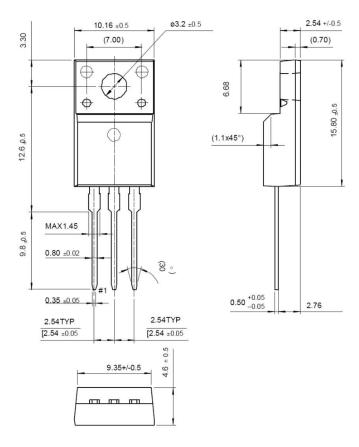
- · Power Factor Correction
- LCD TV Power
- · Full and Half Bridge Power

### **Packing & Order Information**

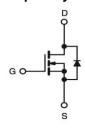
50/Tube; 1,000/Bo



RoHS COMPLIANT



### **Graphic symbol**



## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)						
Symbol	Parameter	Value	Unit			
$V_{\text{DS}}$	Drain-Source Voltage	650	V			
$V_{GS}$	Gate-Source Voltage	±30	V			
1	Drain Current -Continuous (TC=25°C)	12	A			
I <sub>D</sub>	Drain Current -Continuous (TC=100°C)	7.4	A			
$I_{DM}$	Drain Current Pulsed	48	A			
E <sub>AS</sub>	Single Pulsed Avalanche Energy	865	mJ			
E <sub>AR</sub>	Repetitive Avalanche Energy	23.1	mJ			
dV/dt	Peak Diode Recovery dV/dt	4.5	V/ns			
Б	Power Dissipation (TC = 25 °C)	54	W			
$P_D$	Power Dissipation (TC=100°C)	0.43	W/°C			

<sup>·</sup> Drain current limited by maximum junction temperature



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Absolute Maximum Ratings (Tc=25°C unless otherwise noted)						
Symbol	Parameter	Value	Unit			
$T_{J}, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	°C			

### NOTE:

1. Repetitive rating; pulse width limited by maximum junction temperature.

On Characteristics							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
$V_{GS}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	3.0		5.0	V	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V,I <sub>D</sub> =3A		1.95	2.4	Ω	

Off Characteristics						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$V_{GS}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	2.0		4.0	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V,I <sub>D</sub> =3A		0.58	0.65	Ω
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}$ =0 V , $I_D$ =250 $\mu$ A	600			V
ΔBV <sub>DSS</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> =250μA, Referenced to 25°C		0.5		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =600V , V <sub>GS</sub> = 0 V V <sub>DS</sub> =480V , T <sub>C</sub> = 125°C			1 10	μА
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> =30V , V <sub>DS</sub> =0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> =-30V , V <sub>DS</sub> =0 V			-100	nA

Dynamic Characteristics							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz		1760	2290	pF	
Coss	Output Capacitance			182	235	pF	
C <sub>RSS</sub>	Reverse Transfer Capacitance			21	28	pF	

Dynamic Characteristics							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
$t_{d(on)}$	Turn-On Time			30	70	ns	
t <sub>r</sub>	Turn-On Time	$V_{DS}$ =250 V, $I_{D}$ =12A, $R_{G}$ =10 $\Omega$		85	180	ns	
$t_{d(off)}$	Turn-Off Delay Time			140	280	ns	
tf	Turn-Off Fall Time			90	190	ns	
Qg	Total Gate Charge			48	63	nC	
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =480V,I <sub>D</sub> =12A, V <sub>GS</sub> =10 V		8.5		nC	
$Q_{gd}$	Gate-Drain Charge			21		nC	



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Source-Drain Diode Maximum Ratings and Characteristics							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
Is	Continuous Source-Drain Diode Forwa	ard Current			12	_	
I <sub>SM</sub>	ISM Pulsed Source-Drain Diode Forwa			48	- A		
V <sub>SD</sub>	Source-Drain Diode Forward Voltage	I <sub>S</sub> =12A , V <sub>GS</sub> = 0V			1.5	V	
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =12A , V <sub>GS</sub> = 0V		460		ns	
Q <sub>rr</sub>	Reverse Recovery Charge	diF/dt=100A/µs		4.9		μC	



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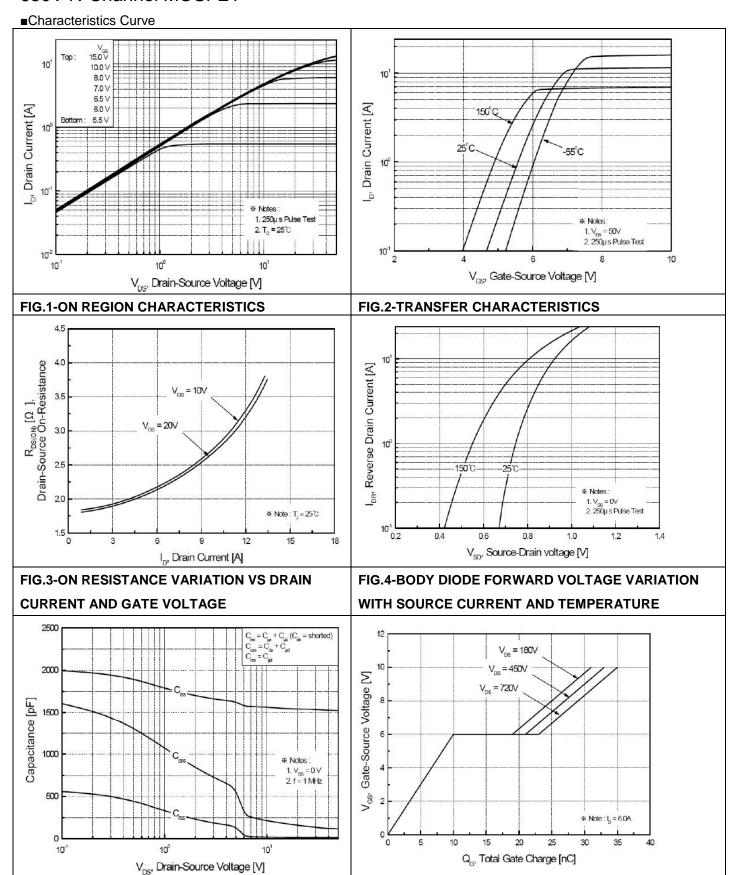


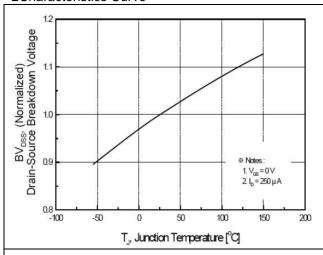
FIG.5-CAPACITANCE CHARACTERISTICS

FIG.6-GATE CHARGE CHARACTERISTICS



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## ■Characteristics Curve



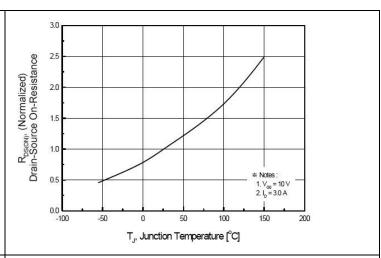


FIG.7-BREAKDOWN VOLTAGE VARIATION VS TEMPERATURE

FIG.8-ON-RESISTANCE VARIATION VS TEMPERATURE

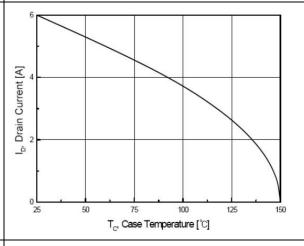


FIG.9-MAXIMUM SAFE OPERATING AREA

FIG.10-MAXIMUM DRAIN CURRENT VS CASE TEMPERATURE

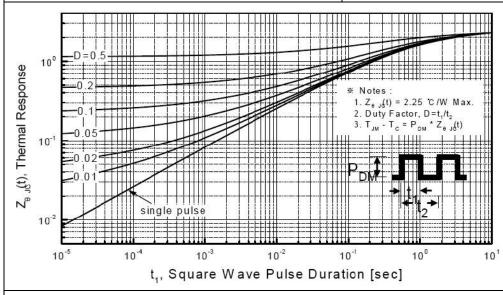


FIG.11-TRANSIENT THERMAL RESPONSE CURVE



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■Characteristics Test Circuit & Waveform

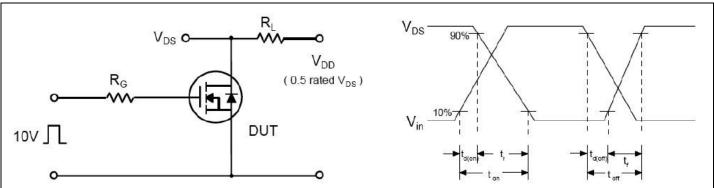


Fig 12. Resistive Switching Test Circuit & Waveforms

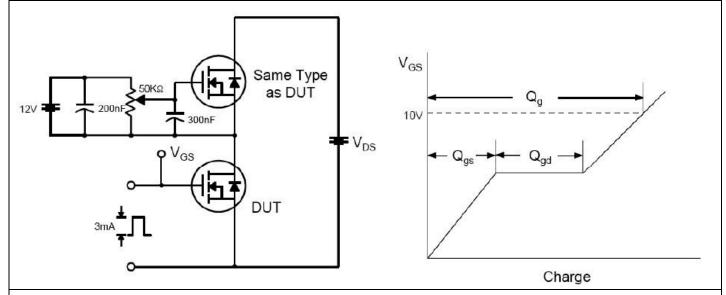
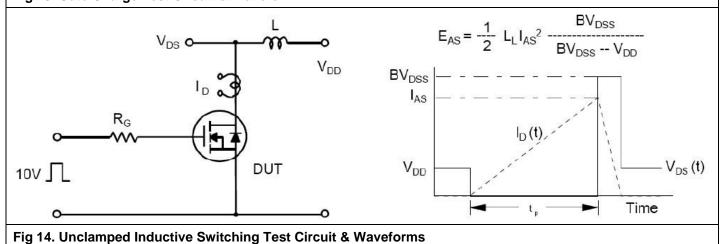


Fig 13. Gate Charge Test Circuit & Waveform





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