

#### Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

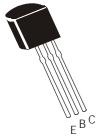




#### NPN SILICON PLANAR EPITAXIAL TRANSISTORS

CN652 / CN653

TO-92 Plastic Package



Use in Wide Variety of Industrial and Consumer Applications Including Lamp and Solenoid Drivers and Audio Amplifier

**Complementary CP752 and CP753** 

## ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C)

DESCRIPTION	SYMBOL	CN652	CN653	UNIT	
Collector Base Voltage	V <sub>CBO</sub>	100	120	V	
Collector Emitter Voltage	$V_{\sf CEO}$	80	100	V	
Emitter Base Voltage	$V_{EBO}$	5	5		
Peak Pulse Current	*I <sub>CM</sub>	6	6		
Collector Current Continuous	I <sub>C</sub>	2	2		
Power Dissipation @ T <sub>a</sub> =25°C	$P_{D}$	0.9	0.9		
Derate Above 25°C		7.2	7.2		
Power Dissipation @ T <sub>a</sub> =25°C	**P <sub>D</sub>	1.1	1.1		
Power Dissipation @ T <sub>c</sub> =25°C	$P_{D}$	2.2	2.2		
Operating and Storage Junction Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	- 65 to	- 65 to +150		

#### **Thermal Resistance**

Junction to Ambient	R <sub>th (j-a) 1</sub>	138.8	°C/W
Junction to Ambient	R <sub>th (j-a) 2+</sub>	113.6	°C/W
Junction to Case	R <sub>th (j-c)</sub>	56.8	°C/W

<sup>\*</sup> Consult safe operating area graph for conditions.

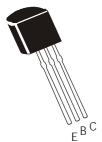
# **ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)**

DESCRIPTION	SYMBOL	TEST CONDITION		MIN	MAX	UNIT
Collector Base Voltage	$V_{CBO}$	$I_{C}=100\mu A, I_{E}=0$	CN652	100		V
			CN653	120		V
Collector Emitter Voltage	$V_{CEO}$	$I_C=1$ mA, $I_B=0$	CN652	80		V
			CN653	100		V
Emitter Base Voltage	$V_{EBO}$	$I_E=100\mu A, I_C=0$		5.0		V
Collector Cut Off Current	I <sub>CBO</sub>	$V_{CB}=80V$ , $I_{E}=0$	CN652		100	nA
		$V_{CB}=80V, I_{E}=0, T_{a}=100^{\circ}C$			10	μΑ
		$V_{CB}$ =100V, $I_{E}$ =0	CN653		100	nA
		$V_{CB}=100V, I_{E}=0, T_{a}=100^{\circ}C$			10	μΑ
Emitter Cut Off Current	I <sub>EBO</sub>	$V_{EB}=4V, I_{C}=0$	_	<u> </u>	100	nA

CN652\_653Rev\_2 211204E

<sup>\*\*</sup>Transistors mounted on printed circuit board. Lead Length 4mm, mounting pad for collector lead min 10mm x 10 mm, copper

<sup>2+</sup> Device mounted on P.C.B with copper equal to 1sq.inch. Minimum



TO-92 Plastic Package

# **ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)**

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Collector Emitter Saturation Voltage	*** V <sub>CE (sat)</sub>	$I_{C}=1A, I_{B}=100mA$		0.3	V
		$I_C=2A$ , $I_B=200mA$		0.5	V
Base Emitter Saturation Voltage	*** V <sub>BE (sat)</sub>	$I_C=1A$ , $I_B=100mA$		1.25	V
Base Emitter on Voltage	*** V <sub>BE (on)</sub>	$I_C=1A, V_{CE}=2V$		1.0	V
DC Current Gain	*** h <sub>FE</sub>	I <sub>C</sub> =50mA,V <sub>CE</sub> =2V	70		
		$I_C=500$ mA, $V_{CE}=2$ V	100	300	
		$I_C=1A$ , $V_{CE}=2V$	55		
		$I_C=2A$ , $V_{CE}=2V$	25		
Transition Frequency	f⊤	$I_C$ =100mA, $V_{CE}$ =5V, f=100MHz	140		MHz
Output Capacitance	$C_{obo}$	$V_{CB}=10V$ , $I_{E}=0$ , $f=1MHz$		30	pF

## **SWITCHING TIMES**

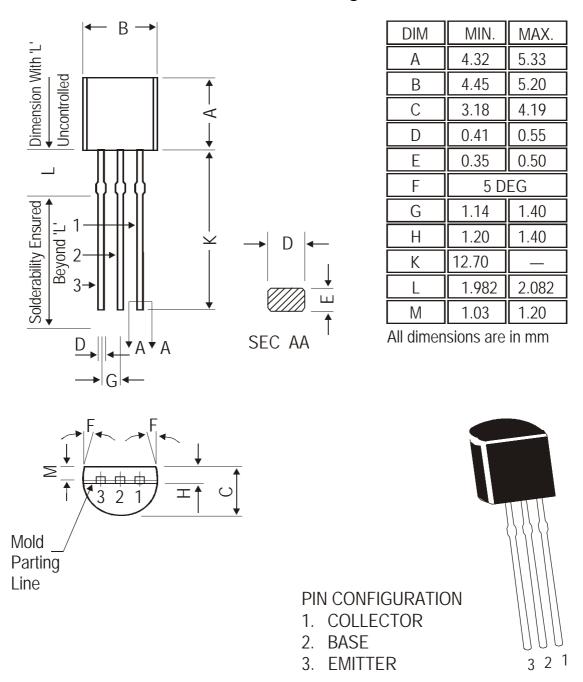
DESCRIPTION	SYMBOL	TEST CONDITION	TYP	UNIT
Turn On Time	t <sub>on</sub>	$I_C$ =500mA, $I_{B1}$ = $I_{B2}$ =50mA,	80	ns
Turn Off Time	t <sub>off</sub>	V <sub>CC</sub> =10V	1200	ns

<sup>\*\*\*</sup> Measured under Pulse conditions. Pulse Width=300ms. Duty Cycle<2%

CN652\_653Rev\_2 211204E

# TO-92 Plastic Package

## **TO-92 Plastic Package**



The TO-92 Package, Tape and Ammo Pack Drawings are correct as on the date of issue/revision of this Data Sheet.

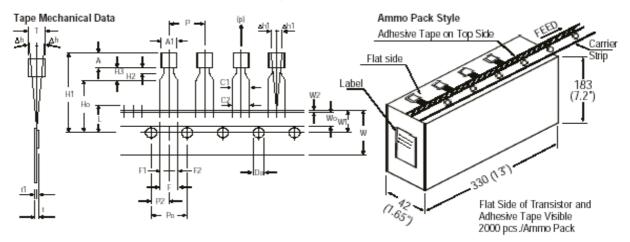
The currently valid dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

### **Packing Details**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details Net Weight/Oty		Size Oty		Size Oty		Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

# TO-92 Plastic Package

# TO-92 Tape and Ammo Pack



#### All dimensions are in mm

		SPECIFICATION		ION		
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	
BODY WIDTH	A1	4.45		5.20		NOTES
BODY HEIGHT BODY THICKNESS	A T	4.32 3.18		5.33 4.19		Maximum alignment deviation between leads will not to be greater than 0.2mm.
PITCH OF COMPONENT	Р		12.7		± 1.0	Maximum non-cumulative variation
*1FEED HOLE PITCH *2 FEED HOLE CENTRE TO	Po		12.7		± 0.3	between tape feed holes shall not exceed 1 mm in 20 pitches.
COMPONENT CENTRE	P2		6.35		± 0.4	Holddown tape will not exceed beyond
DISTANCE BETWEEN OUTER LEADS	F		5.08		+ 0.6 - 0.2	the edge(s) of carrier tape and there shall be no exposure of adhesive.
*3 COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0		There will be no more than three (3)
*4 COMPONENT ALIGNMENT FRONT VIEW	∆h1		0	1.3		consecutive missing components in a tape.
TAPE WIDTH	W		18		± 0.5	'
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	<ol><li>A tape trailer, having at least three feed holes are provided after the last</li></ol>
HOLE POSITION	W1		9		+ 0.7 - 0.5	component in a tape.
HOLD-DOWN TAPE POSITION	W2	0.0		0.7		Splices should not interfere with the
LEAD WIRE CLINCH HEIGHT	Ho		16		± 0.5	sprocket feed holes.
COMPONENT HEIGHT	H1			24.0		
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	Do		4		± 0.2	REMARKS
*5 TOTAL TAPE THICKNESS	t			1.2		
LEAD - TO - LEAD DISTANCE	F1, F2	2.40		2.70	- 0.1	*1 Cumulative pitch error 1.0 mm/20 pitch
STAND OFF	H2	0.45		1.45	- 0. 1	*2 To be measured at bottom of clinch
CLINCH HEIGHT	Н3			3.0		*3 At top of body
LEAD PARALLELISM	C1 - C2			0.22		*4 At top of body
PULL - OUT FORCE	(p)	6N				*5 t1 0.3 – 0.6 mm

CN652\_653Rev\_2 211204E

Customer Notes CN652 / CN653

TO-92 Plastic Package

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of
Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119
email@cdil.com www.cdilsemi.com