



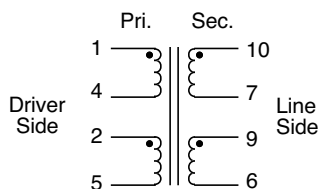
- Designed to meet Supplementary Insulation Requirement of IEC950 for 250 VAC Working Voltage
- Excellent THD Characteristic
- Isolation : 2000 Vrms
- SMD Package

Electrical Parameters @ 25° C

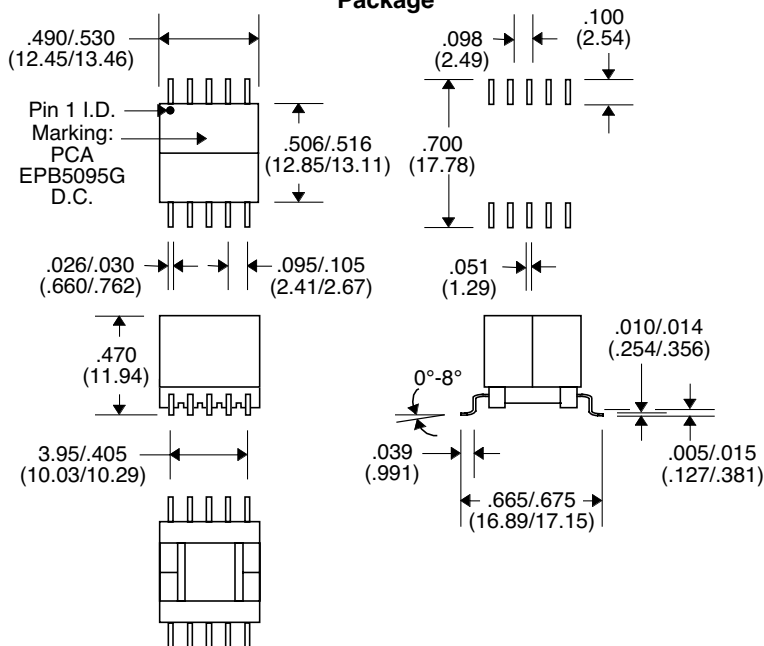
Pri. OCL ($\mu\text{H} \pm 10\%$)	Sec. OCL ($\mu\text{H} \pm 10\%$)	Leakage Inductance (μH Max.)		Interwinding Capacitance (pF Max.)		Insertion Loss (dB Max.)	Total Harmonic Distortion (dB Max.)	Longitudinal Balance (dB Min.)	Turns Ratio (Pri.:Sec. $\pm 1\%$)	
@ 100 KHz, 0.1 Vrms	@ 100 KHz, 0.1 Vrms	@ 100 KHz, 0.1 Vrms		@ 100 KHz, 0.1 Vrms		35 KHz to 1.0 MHz	@ 100 KHz, 15 V p-p	35 KHz to 650 KHz		
55	437	1.5	10	10	20	-0.5	-80	-60	1:2.9	1:1
Pins 1-4	Pins 10-6 w/ 7, 9 shorted	Pins 1-4 w/ 2-5 open, 6,7,9,10 shorted	Pins 2-5 w/ 1-4 open, 6,7,9,10 shorted	Pins 1-4 to 10-6 w/ 7, 9 shorted	Pins 2-5 to 10-6 w/ 7, 9 shorted	Pins 10-6 to 1-5 w/ 7, 9 & 4, 2 shorted	Pins 10-6 w/ 7, 9 shorted	Pins 1-4 to 10-6 w/ 7, 9 shorted	1-4:10-6 w/ 7, 9 shorted	2-5:10-6 w/ 7, 9 shorted

- Secondary Wdg. Impedance : 100 Ω • DCR : 0.5 Ω @ Pins 1-4, 2.0 Ω @ Pins 2-5, 2.0 Ω @ Pins 10-6 w/ 7, 9 shorted •
- DC Unbalanced Current : 2.0 mA @ Pins 1-5 w/ 2, 4 shorted •

Schematic



Package



Documentor/Date

✓'d By

Project Engineer/Date

Engineering/Date

Quality Assurance/Date

Unless Otherwise Specified Dimensions are in Inches /mm $\pm .010$ /.25