

Crystal Clock Oscillators

clk osc

Generic part number
Frequency range
Standard frequencies (MHz)
Frequency stability

over all conditions
Operating Conditions
Operating temperature

Input voltage (VDD)
Stand-by control voltage (Pin#1)

Absolute Max. Ratings
Supply voltage
Storage temperature
Input current
(Pin#1=Open or ViH)
Stand-by current
Output
Symmetry
Rise and fall times
"0" level
"1" level
Load
Disable delay time
Enable delay time
Startup time
Aging (non operation)
SSB phase noise
Reflow condition

32SMOHG

Lid	Metal
Base	Ceramic
Sealing	AuSn
Terminal	Tungsten (metalized)
Terminal plating	Gold / Nickel (surface) / (under)
RoHS	