

# UNISONIC TECHNOLOGIES CO., LTD

5N60K-MT **Power MOSFET** 

# 5A, 600V N-CHANNEL POWER MOSFET

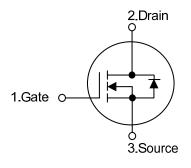
#### **DESCRIPTION**

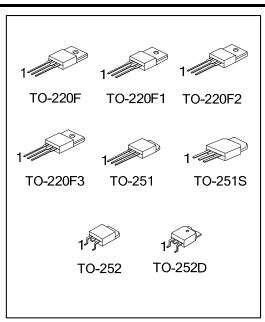
The UTC 5N60K-MT is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

#### **FEATURES**

- \*  $R_{DS(ON)}$  < 2.2 $\Omega$  @  $V_{GS}$  =10V,  $I_{D}$  = 2.5A
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness

#### **SYMBOL**

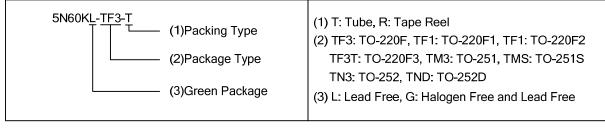




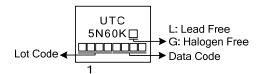
#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Deelsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
5N60KL-TF3-T	5N60KG-TF3-T	TO-220F	G	D	S	Tube	
5N60KL-TF1-T	5N60KG-TF1-T	TO-220F1	G	D	S	Tube	
5N60KL-TF2-T	5N60KG-TF2-T	TO-220F2	G	D	S	Tube	
5N60KL-TF3T-T	5N60KG-TF3T-T	TO-220F3	G	D	S	Tube	
5N60KL-TM3-T	5N60KG-TM3-T	TO-251	G	D	S	Tube	
5N60KL-TMS-T	5N60KG-TMS-T	TO-251S	G	D	S	Tube	
5N60KL-TN3-R	5N60KG-TN3-R	TO-252	G	D	S	Tape Reel	
5N60KL-TND-R	5N60KG-TND-R	TO-252D	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



# ■ MARKING



# ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	٧
Gate-Source Voltage		$V_{GSS}$	±30	٧
Continuous Drain Current		I <sub>D</sub>	5	Α
Pulsed Drain Current (Note 2)		I <sub>DM</sub> 20		Α
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	220	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220F/TO-220F1 TO-220F3		36	W
	TO-220F2	$P_{D}$	38	W
	TO-251/TO-251S TO-252/TO-252D		54	W
Junction Temperature		TJ	+150	°C
Operation Temperature		T <sub>OPR</sub>	-55 ~ +150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Pulse width limited by  $T_{J(\mbox{\scriptsize MAX})}$
- 3. L = 17.6mH,  $I_{AS}$  = 5A,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25°C
- 4.  $I_{SD} \le 5A$ , di/dt  $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$

# ■ THERMAL DATA

PARAMETE <u>R</u>		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220F/TO-220F1/ TO-220F2/TO-220F3	0	62.5	°C/W	
	TO-251/TO-251S TO-252/TO-252D	$\Theta_{JA}$	160	°C/W	
Junction to Case	TO-220F/TO-220F1 TO-220F3		3.47	°C/W	
	TO-220F2	$\theta_{JC}$	3.28	°C/W	
	TO-251/TO-251S TO-252/TO-252D		2.30	°C/W	

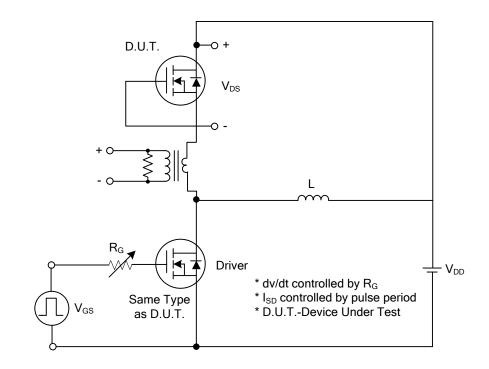
# ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	600			V	
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =600V, V <sub>GS</sub> = 0V			1	μΑ	
Gate-Source Leakage Current	Forward		$V_{GS}$ =30V, $V_{DS}$ = 0V			100	nA	
	Reverse		$V_{GS} = -30V, V_{DS} = 0V$	-		-100	IIA	
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	I <sub>D</sub> =250μA, Referenced to 25℃		0.6		V/°C	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V	
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	$V_{GS} = 10V, I_D = 2.5A$		1.5	2.2	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance	nput Capacitance		$V_{DS} = 25V, V_{GS} = 0V,$		460	620	pF	
Output Capacitance		Coss	f = 1.0MHz		70	90	pF	
Reverse Transfer Capacitance		$C_{RSS}$	1 - 1.01/11/12		8	12	pF	
SWITCHING CHARACTERISTIC	S							
Turn-On Delay Time		t <sub>D(ON)</sub>			50		ns	
Turn-On Rise Time		t <sub>R</sub>	$V_{DD}$ =30V, $I_{D}$ =0.5A, $R_{G}$ =25 $\Omega$		60		ns	
Turn-Off Delay Time		t <sub>D(OFF)</sub>	(Note 1, 2)		120		ns	
Turn-Off Fall Time		t <sub>F</sub>			35		ns	
Total Gate Charge		$Q_{G}$	V <sub>DS</sub> =50V, I <sub>D</sub> =1.3A, V <sub>GS</sub> =10V		18		nC	
Gate-Source Charge		$Q_{GS}$	(Note 1, 2)		6.7		nC	
Gate-Drain Charge		$Q_GD$	(14010-1, 2)		4.5		nC	
DRAIN-SOURCE DIODE CHARA	CTERISTIC	CS AND MAXII	MUM RATINGS					
Drain-Source Diode Forward Voltage		$V_{SD}$	$V_{GS} = 0 \text{ V}, I_{S} = 5A$			1.4	V	
Maximum Continuous Drain-Source Diode		la				5	Α	
Forward Current		Is				5	^	
Maximum Pulsed Drain-Source Diode		I <sub>SM</sub>				20	Α	
Forward Current		'SIVI				20	/ \	

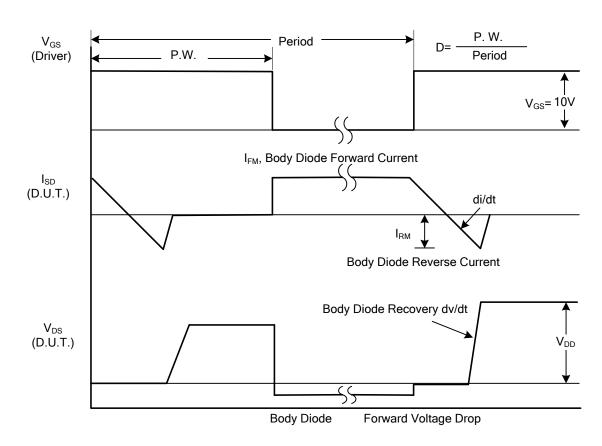
Note: 1. Pulse Test: Pulse width  $\leq$  300 $\mu$ s, Duty cycle  $\leq$  2%

<sup>2.</sup> Essentially independent of operating temperature

# ■ TEST CIRCUITS AND WAVEFORMS

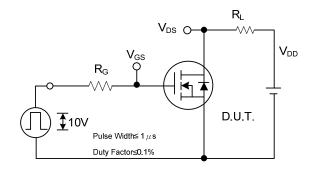


Peak Diode Recovery dv/dt Test Circuit

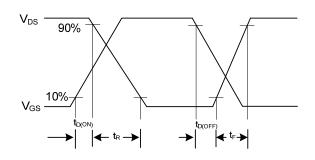


Peak Diode Recovery dv/dt Waveforms

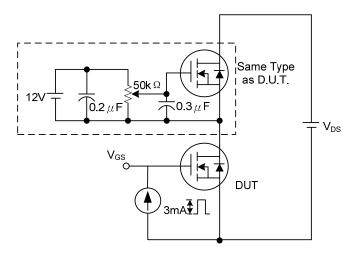
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

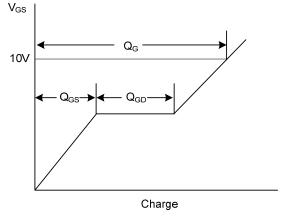


**Switching Test Circuit** 



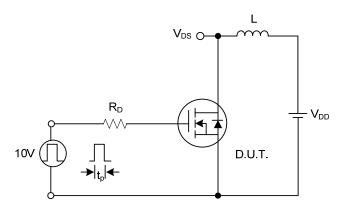
**Switching Waveforms** 



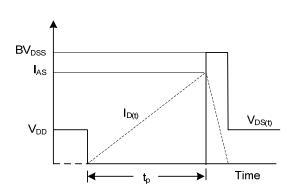


**Gate Charge Test Circuit** 

**Gate Charge Waveform** 



**Unclamped Inductive Switching Test Circuit** 



**Unclamped Inductive Switching Waveforms** 

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