

Selected Electrical Specifications

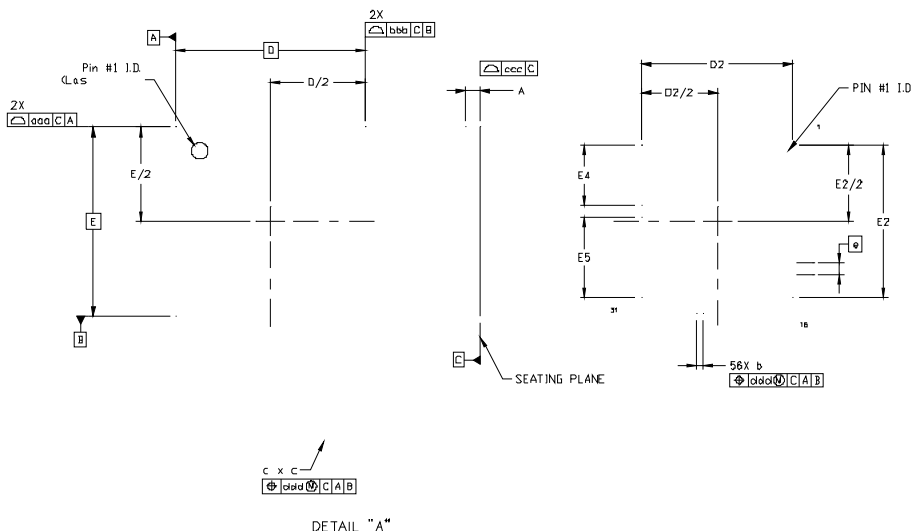
Parameter	Symbol	Test Condition	Min	Typical	Max	Unit
Ambient Temperature	T_A	F-Grade	0	25	70	°C
		G-Grade	-40	25	85	°C
Supply Voltage	V_{DD}		3.13	3.3	3.47	V
Battery Voltage	V_{BAT}		-15	—	-110/-140	V
Maximum Loop Resistance (loop + load)	R_{LOOP}	$I_{LOOP}=18\text{ mA}$, $V_{BAT} = -52\text{ V}$	—	—	2000	
DC Differential Output Resistance	R_{DO}	$I_{LOOP} < I_{LIM}$	160	—	640	
Idle Channel Noise		C-Message weighted	—	8	12	dBrnC
PSRR from V_{DD}		RX and TX, dc to 3.4 kHz	—	55	—	dB
Longitudinal to Metallic/PCM Balance (forward or reverse)		200 Hz to 1 kHz	58	60	—	dB
		1 kHz to 3.4 kHz	53	58	—	dB
Metallic/PCM to Longitudinal Balance		200 Hz to 3.4 kHz	40	—	—	dB
Longitudinal Impedance		200 Hz to 3.4 kHz at TIP or RING	—	50	—	
Longitudinal Current per Pin		Active off-hook 200 Hz to 3.4 kHz	—	25	—	mA
DC Feed Current			—	—	45	mA
2-Wire Return Loss		200 Hz to 3.4 kHz	26	30	—	dB
Transhybrid Balance		300 Hz to 3.4 kHz	26	30	—	dB
Thermal Resistance (QFN-60)	JA		—	42	—	°C/W

Ordering Guide

FXS Pin	Description	Max Vbat	Temperature
Si32260-C-FM	Dual FXS, wideband capable	-110 V	0 to 70 °C
Si32260-C-GM	Dual FXS, wideband capable	-110 V	-40 to 85 °C
Si32261-C-FM	Dual FXS, wideband capable	-140 V	0 to 70 °C
Si32261-C-GM	Dual FXS, wideband capable	-140 V	-40 to 85 °C

***Note:** Adding the suffix "R" to the part number (e.g., Si32261-C-FMR) denotes tape and ree

Package Information 60-pin QFN



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