# 3SK227

## Silicon N-Channel 4-pin MOS FET

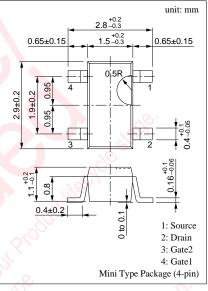
#### For VHF amplification

#### Features

- Low noise-figure (NF)
- Large power gain PG
- Mini-type package, allowing downsizing of the sets and automatic insertion through the tape/magazine packing.

$\blacksquare \text{ Absolute Waximum Ratings (1a = 25 C)}$						
Parameter	Symbol	Ratings	Unit			
Drain to Source voltage	V <sub>DS</sub>	15	V			
Gate 1 to Source voltage	V <sub>G1S</sub>	±8	V			
Gate 2 to Source voltage	V <sub>G2S</sub>	±8	v			
Drain current	ID	±30	mA			
Allowable power dissipation	P <sub>D</sub>	200	mW			
Channel temperature	T <sub>ch</sub>	150	°C			
Storage temperature	T <sub>stg</sub>	-55 to +150	°C			

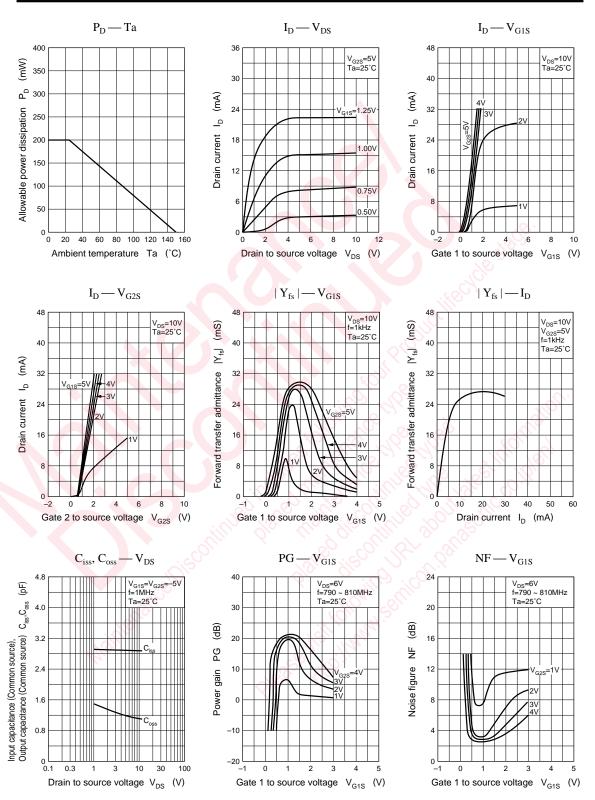
#### Absolute Maximum Ratings ( $Ta = 25^{\circ}C$ )



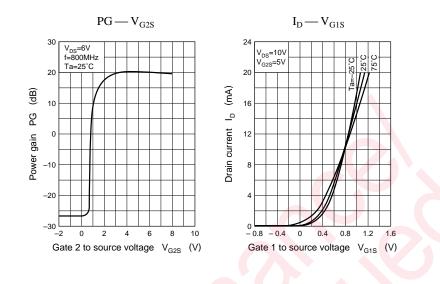
### Marking Symbol: CX

#### **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain current	I <sub>DS</sub>	$V_{DS} = 10V, V_{G1S} = 1V, V_{G2S} = 5V$	×6 ~	5, 20	25	mA
Gate 1 cut-off current	I <sub>G1SS</sub>	$V_{DS} = V_{G2S} = 0, V_{G1S} = \pm 8V$		R	±20	nA
Gate 2 cut-off current	I <sub>G2SS</sub>	$V_{DS} = V_{G1S} = 0, V_{G2S} = \pm 8V$	K.	3	±20	nA
Drain to Source voltage	V <sub>DSX</sub>	$I_D = 50\mu A, V_{G1S} = -5V, V_{G2S} = 0$	15			V
Gate 1 to Source cut-off voltage	V <sub>G1SC</sub>	$V_{DS} = 10V, V_{G2S} = 5V, I_D = 100\mu A$	-1.5		1	V
Gate 2 to Source cut-off voltage	V <sub>G2SC</sub>	$V_{DS} = 10V, V_{G1S} = 5V, I_D = 100\mu A$	0		1	V
Forward transfer admittance	$ \mathbf{Y}_{\mathrm{fs}} $	$V_{DS} = 10V, I_D = 10mA, V_{G2S} = 5V, f = 1kHz$	21	26	31	mS
Input capacitance (Common Source)	C <sub>iss</sub>	V = 10V V = V = 5V		2.8		pF
Output capacitance (Common Source)	C <sub>oss</sub>	$V_{DS} = 10V, V_{G1S} = V_{G2S} = -5V$		1.1		pF
Reverse transfer capacitance (Common Source)	C <sub>rss</sub>	f = 1MHz		0.02		pF
Power gain	PG	$V_{DS} = 6V, I_D = 8mA, V_{G2S} = 4V$	18.5	20.5		dB
Noise figure	NF	f = 790 to 810MHz (Sweep)		2.5	3.7	dB



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