TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

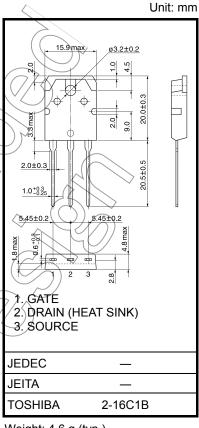
2SK3845

Switching Regulator, DC-DC Converter Applications and Motor Drive Applications

- Low drain-source ON resistance: $R_{DS (ON)} = 4.7 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: |Y_{fs}| = 88 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 60 V)
- Enhancement model: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit |
|-------------------------|----------------------------------|------------------|------------|----------|
| Drain-source voltage | | V _{DSS} | 60 | \ |
| Drain-gate voltage (Ro | $_{\rm SS} = 20 \; \rm k\Omega)$ | V_{DGR} | 60 | V |
| Gate-source voltage | | V _{GSS} | ±20 | V |
| Drain current | DC (Note 1) | I _D | 70 | Α |
| | Pulse (Note 1) | I _{DP} | 280 | |
| Drain power dissipation | n (Tc = 25°C) | PD | 125 | <\\w |
| Single pulse avalanche | e energy (Note 2) | EAS | 328 | mJ |
| Avalanche current | | TAR | 70 〈 | \ A |
| Repetitive avalanche e | energy (Note 3) | EAR | 12.5 | /mJ |
| Channel temperature | ((| 7 Tch | 150 | \~c |
| Storage temperature ra | ange | Tstg | -55 to 150 | →°C |



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

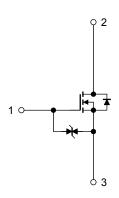
| Characteristics | Symbol | Max | Unit |
|--|------------------------|-----|------|
| Thermal resistance, channel to case | R _{th (ch-c)} | 1.0 | °C/W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 50 | °C/W |

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD}=25$ V, $T_{ch}=25^{\circ}C$ (initial), $L=91~\mu\text{H},~R_{G}=25~\Omega,~I_{AR}=70~\text{A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



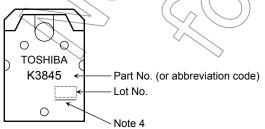
Electrical Characteristics (Ta = 25°C)

| Chara | acteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|---------------|----------------------|--|------------|-------|------------|------|
| Gate leakage cur | rent | I _{GSS} | $V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±10 | μΑ |
| Drain cut-OFF cu | rrent | I _{DSS} | V _{DS} = 60V, V _{GS} = 0 V | _ | _ | 100 | μΑ |
| Drain-source breakdown voltage | | V (BR) DSS | I _D = 10mA, V _{GS} = 0 V | 60 | _ | _ | ٧ |
| | | V (BR) DSX | $I_D = 10 \text{mA}, \ V_{GS} = -20 \ \text{V}$ | 35 | _ | _ | |
| Gate threshold vo | oltage | V _{th} | V _{DS} = 10 V, I _D = 1 mA | 2.0 |) / _ | 4.0 | V |
| Drain-source ON | resistance | R _{DS} (ON) | V _{GS} = 10 V, I _D = 35 A | \nearrow | 4.7 | 5.8 | mΩ |
| Forward transfer | admittance | Y _{fs} | V _{DS} = 10 V, I _D = 35 A | 44 | 88 | _ | S |
| Input capacitance | | C _{iss} | | | 12400 | _ | |
| Reverse transfer capacitance | | C _{rss} | V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz | <i>_</i> | 700 | _ | pF |
| Output capacitance | | Coss | | _ | 1100 | _ | |
| Switching time | Rise time | t _r | 10 V ID = 35 A VOUT | - | 17 | <u>/</u> / | |
| | Turn-on time | t _{on} | 0 V R _L = 0.86 Ω | | 44 |) — | ns |
| | Fall time | t _f | | | 35 | _ | |
| | Turn-off time | t _{off} | Duty \leq 1%, $t_W = 10 \mu s$ $V_{DD} = 30 V$ | | 200 | | |
| Total gate charge (gate-source plus gate-drain) | | Qg | |) | 196 | | |
| Gate-source charge | | Qgs | $V_{DD} \simeq 48 \text{ V}, V_{GS} \neq 10 \text{ V}, I_D = 70 \text{ A}$ | _ | 148 | | nC |
| Gate-drain ("miller") charge | | Qgq | | _ | 48 | _ | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | (7)\\ - | _ | _ | 70 | Α |
| Pulse drain reverse current (Note 1) | I _{DRP} | <u> </u> | _ | _ | 280 | Α |
| Forward voltage (diode) | VDSF | $I_{DR} = 70 \text{ A}, V_{GS} = 0 \text{ V}$ | _ | _ | -1.5 | ٧ |
| Reverse recovery time | tir | $I_{DR} = 70 \text{ A}, V_{GS} = 0 \text{ V},$ | _ | 70 | _ | ns |
| Reverse recovery charge | Qrr | dl _{DR} /dt = 50 A/μs | _ | 77 | _ | nC |

Marking



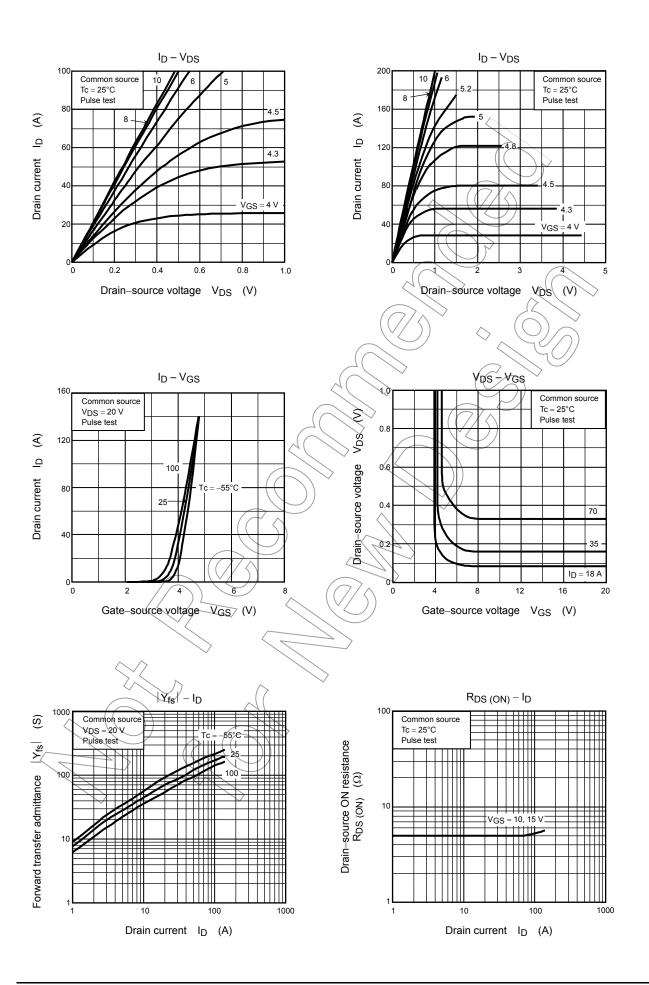
Note 4: A line under a Lot No. identifies the indication of product Labels.

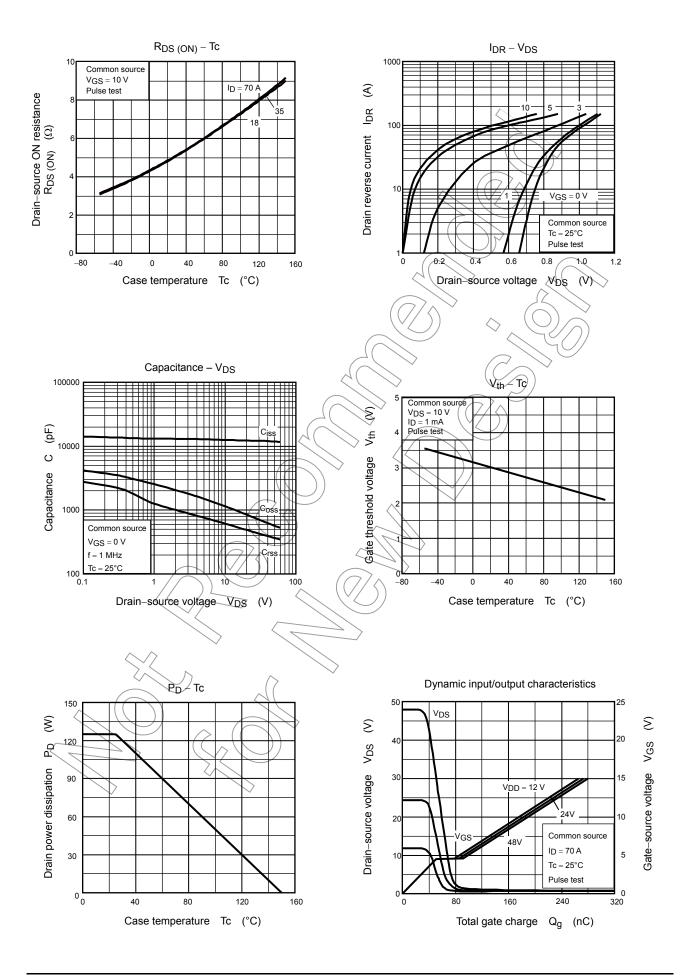
Not underlined: [[Pb]]/INCLUDES > MCV

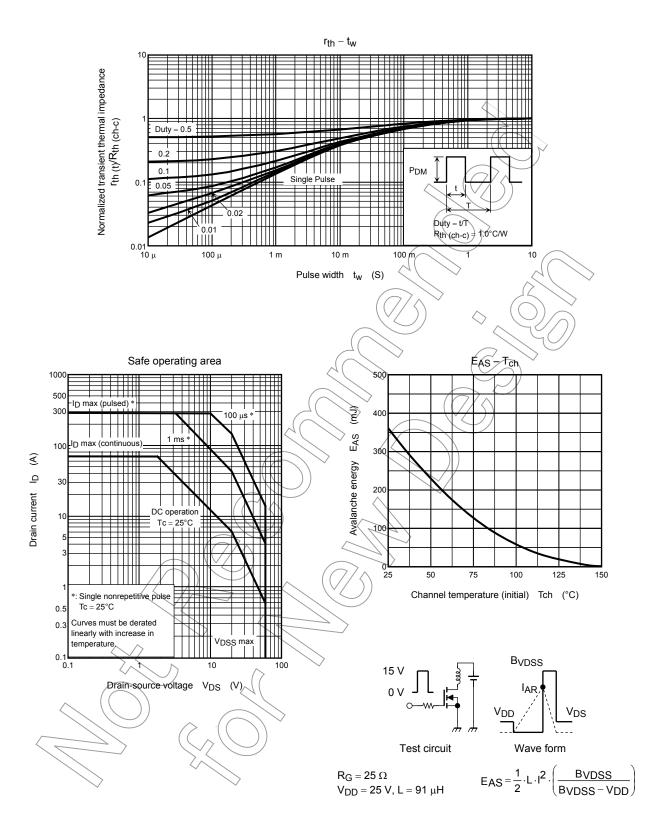
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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