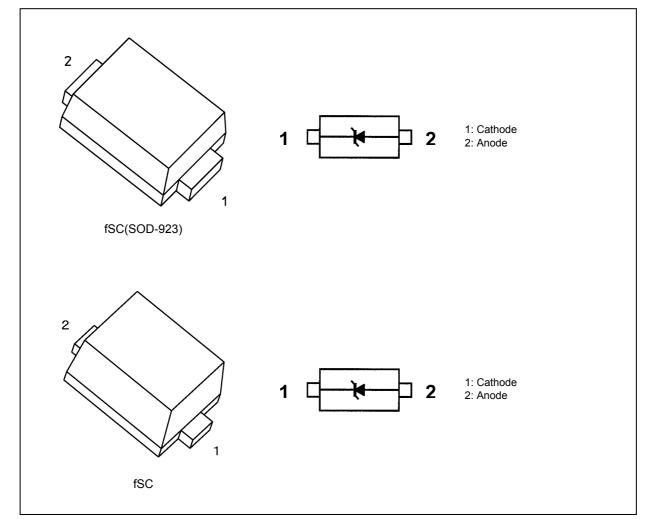
ESD Protection Diodes Silicon Epitaxial Planar

# **DF2S6.8FS**

## 1. Applications

- ESD Protection
- Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

### 2. Packaging and Internal Circuit



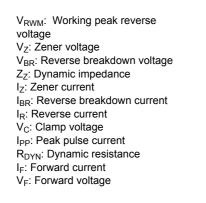
#### 3. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25^{\circ}$ C)

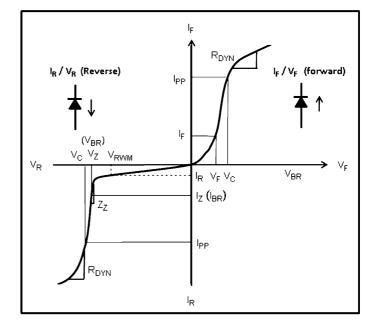
Characteristics	Symbol	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2)(Contact)	V <sub>ESD</sub>	±30	kV
Junction temperature	Тj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	C°

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## 4. Electrical Characteristics (Unless otherwise specified, Ta = 25°C)





#### Fig. 4.1 Definitions of Electrical Characteristics

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	V <sub>RWM</sub>		—	_		5	V
Zener voltage (Reverse breakdown voltage)	V <sub>Z</sub> (V <sub>BR</sub> )		I <sub>Z</sub> = 5 mA (I <sub>BR</sub> )	6.4	6.8	7.2	V
Dynamic impedance	ZZ		I <sub>Z</sub> = 5 mA (I <sub>BR</sub> )		—	30	Ω
Reverse current	I <sub>R</sub>		V <sub>RWM</sub> = 5 V	_	—	0.5	μA
Clamp voltage	V <sub>C</sub>	(Note 1)	I <sub>PP</sub> = 1 A	_	9	—	V
Dynamic resistance	R <sub>DYN</sub>	(Note 2)	—	_	0.7	_	Ω
Total capacitance	Ct		V <sub>R</sub> = 0 V, f = 1 MHz	_	25	_	pF

Note 1: Based on IEC61000-4-5 8/20  $\mu s$  pulse.

Note 2: TLP parameter: Z0 = 50  $\Omega$ , tp = 100 ns, tr = 300 ps, averaging window: t1 = 30 ns to t2 = 60 ns,

extraction of dynamic resistance using a least-squares fit of TLP characteristics at IPP between 3 A to 8 A.

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### 5. Guaranteed ESD Protection (Note)

Test Condition	ESD Protection		
IEC61000-4-2 (Contact discharge)	±30 kV		

Note: Criterion: No damage to devices.

# 6. Marking

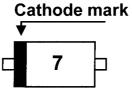
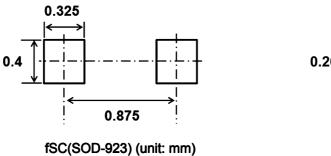
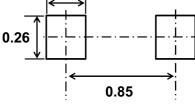


Fig. 6.1 Marking

# 7. Land Pattern Dimensions (for reference only)



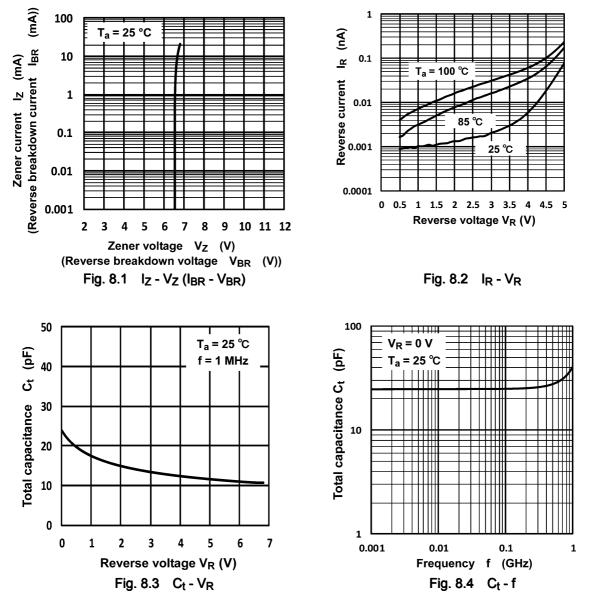


0.21

fSC (unit: mm)

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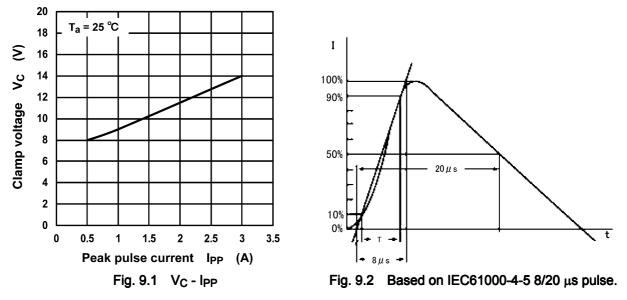
#### 8. Characteristics Curves (Note)



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

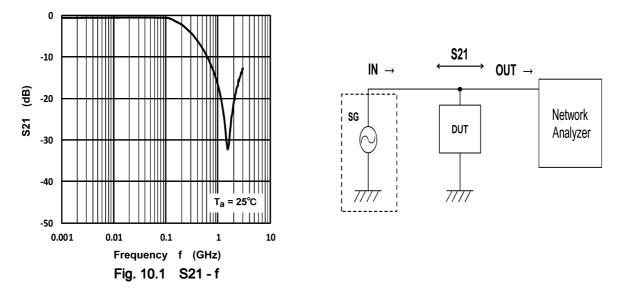
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9. Clamp Voltage V<sub>C</sub> - Peak Pulse Current (IPP) (Note)



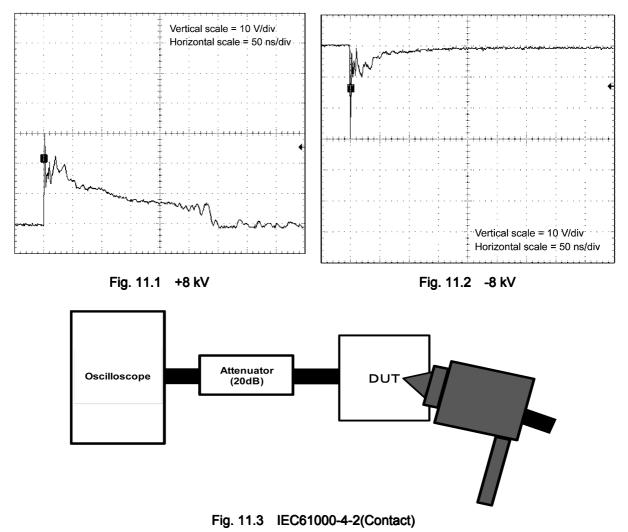
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### 10. Insertion Loss (S21) (Note)



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

# 11. ESD Clamp Waveform (Note)

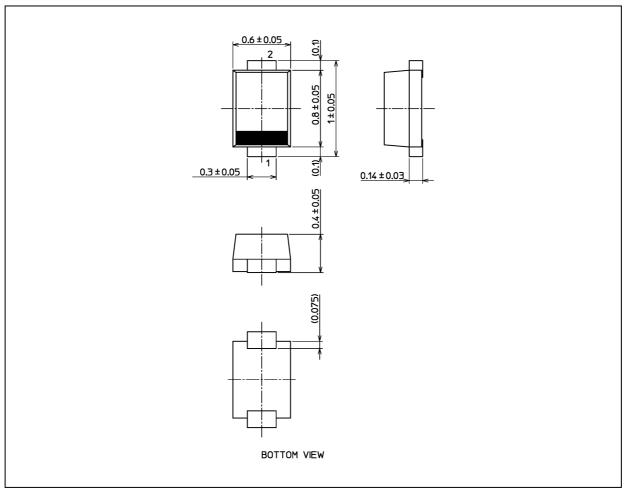


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



#### **Package Dimensions**

Unit: mm



The shapes and dimensions of the package vary, depending on the manufacturing plant. For details, contact the Toshiba sales representative.

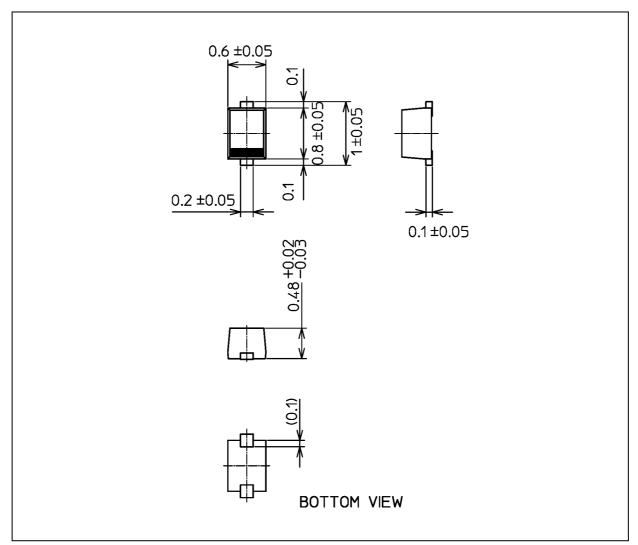
Weight: 0.55 mg (typ.)

Package Name(s)
TOSHIBA: 1-1AH1A
Nickname: fSC(SOD-923)



# Package Dimensions

Unit: mm



The shapes and dimensions of the package vary, depending on the manufacturing plant. For details, contact the Toshiba sales representative.

Weight: 0.6 mg (typ.)

Package Name(s)				
TOSHIBA: 1-1L1S				
Nickname: fSC				

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