RoHS

HALOGEN FREE

GREEN

(5-2008)



Vishay Semiconductors

Silicon PIN Photodiode



DESCRIPTION

VEMD10940F is a high speed and high sensitive PIN photodiode in a miniature side looking, surface mount package (SMD) with daylight blocking filter. Filter is matched with IR emitters operating at wavelength of 830 nm to 950 nm. The photo sensitive area of the chip is 0.23 mm².

FEATURES

- Package type: Surface mount
- Package form: Side view
- Dimensions (L x W x H in mm): 3 x 2 x 1
- High radiant sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm IR emitters
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 75^{\circ}$
- Package matched with IR emitter VSMB10940 and VSMG10850
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- · High speed photo detector
- · Infrared remote control
- Infrared data transmission
- Photo interrupters
- · IR touch panels

PRODUCT SUMMARY				
COMPONENT	I _{ra} (μΑ)	φ (deg)	λ _{0.5} (nm)	
VEMD10940F	3	± 75	780 to 1050	

Note

· Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VEMD10940F	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	Side view		

Note

MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	60	V	
Power dissipation	T _{amb} ≤ 25 °C	P _V	104	mW	
Junction temperature		T _j	100	°C	
Operating temperature range		T _{amb}	- 40 to + 100	°C	
Storage temperature range		T _{stg}	- 40 to + 100	°C	
Soldering temperature	Acc. reflow solder profile fig. 7	T _{sd}	260	°C	
Thermal resistance junction/ambient	Acc. J-STD-051	R _{thJA}	450	K/W	



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BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 50 mA	V _F		1		V
Breakdown voltage	I _R = 100 μA, E = 0	V _(BR)	32			V
Reverse dark current	V _R = 10 V, E = 0	I _{ro}		1	10	nA
Diode capacitance	$V_R = 0 \text{ V, } f = 1 \text{ MHz, } E = 0$	C _D		4		pF
	V _R = 5 V, f = 1 MHz, E = 0	C _D		1.3		pF
Open circuit voltage	$E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	TK _{Vo}		- 2.6		mV/K
Short circuit current	$E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$	I _k		3		μΑ
Temperature coefficient of Ik	$E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$	TK _{lk}		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$, $V_R = 5 \text{ V}$	I _{ra}	2	3	4	μΑ
Angle of half sensitivity		φ		± 75		deg
Wavelength of peak sensitivity		λρ		920		nm
Range of spectral bandwidth		λ _{0.5}		780 to 1050		nm
Rise time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t _r		100		ns
Fall time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t _f		100		ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

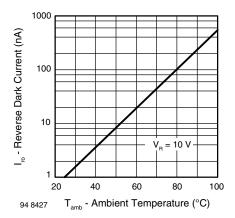


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

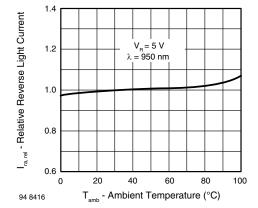


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

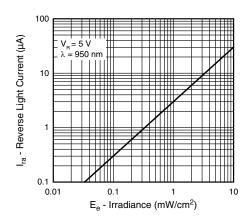


Fig. 3 - Reverse Light Current vs. Irradiance

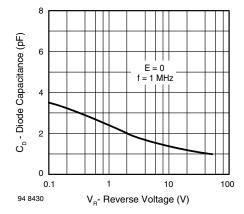


Fig. 4 - Diode Capacitance vs. Reverse Voltage



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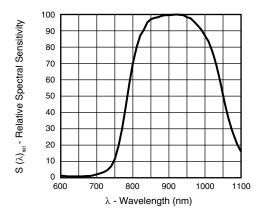


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

REFLOW SOLDER PROFILE

255 - 260 °C 217 °C 3 °C/SEC. MAX. 200 °C 150 °C 3 °C/SEC. MAX.

Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020D

TIME

60 - 120 SEC

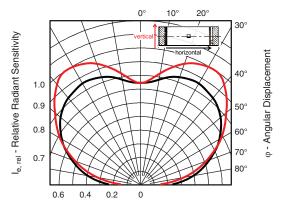


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 3, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

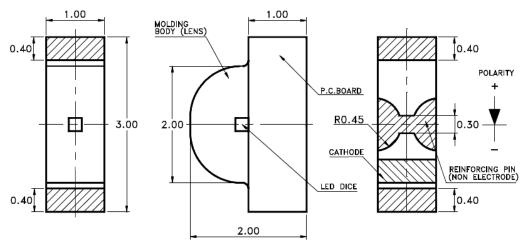
60 SEC. MAX.



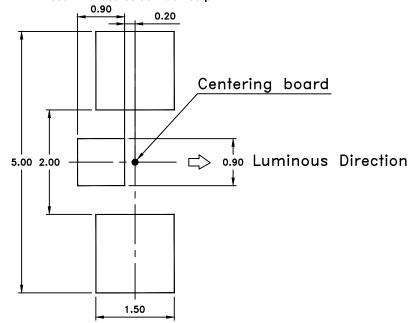
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PACKAGE DIMENSIONS in millimeters: **VEMD10940F**



Recommended Solder Pad Footprint

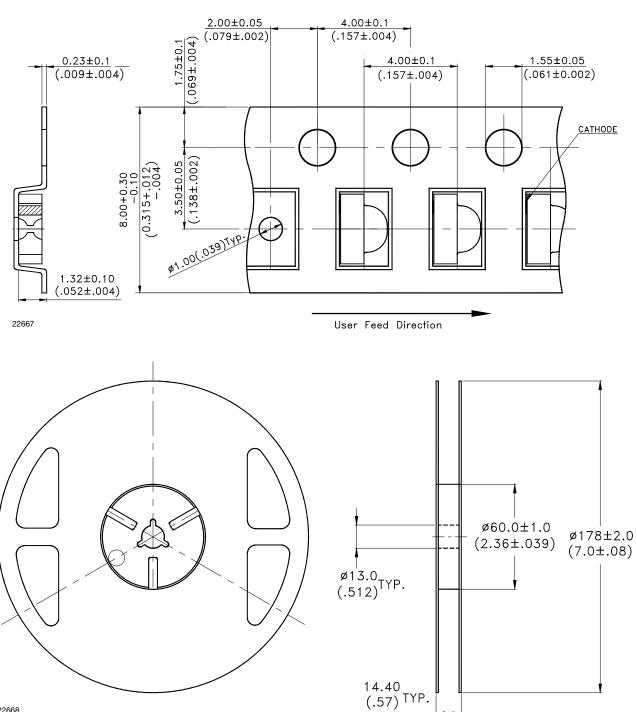




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TAPING AND REEL DIMENSIONS in millimeters: VEMD10940F





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Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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Revision: 02-Oct-12 Document Number: 91000

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