

6A DIODESTAR RECTIFIER

Features

- DIODESTARTM is a Proprietary Process for High Voltage Rectifiers which Delivers:
 - Ultra-Fast Reverse Recovery (t_{rr} < 30ns) Giving a Rapid Switching Response
 - Soft Recovery for Low EMI Noise
 - Excellent High Temperature Stability
 - High Forward Surge Capability
 - Enables High Efficiency as the Boost Diode in PFC Circuits
- Lead Free Finish, RoHS Compliant (Note 1)

Mechanical Data

Package Pin Out Configuration

- Case: DPAK (TO252-3L)
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.4 grams (approximate)



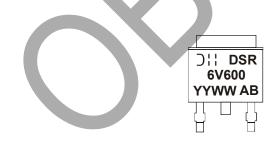
Ordering Information (Note 2)

	000000		
Part Number		Case	Packaging
DSR6V600D1-13		DPAK (TO252-3L)	2500 pieces/reel

Notes: 1. No purposefully added lead.

2. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



DSR6V600 = Product Type Marking Code AB = Foundry and Assembly Code YYWW = Date Code Marking YY = Last two digits of year (ex: 08 = 2008) WW = Week (01 - 53)





Maximum Ratings @T_A = 25°C unless otherwise specified Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%. Characteristic Symbol Value Unit Peak Repetitive Reverse Voltage V_{RRM} Working Peak Reverse Voltage 600 V V_{RWM} DC Blocking Voltage V_{RM} Average Rectified Output Current 6 А I_{O} Non-Repetitive Peak Forward Surge Current 8.3ms 60 А IFSM Single Half Sine-Wave Superimposed on Rated Load

Thermal Characteristics

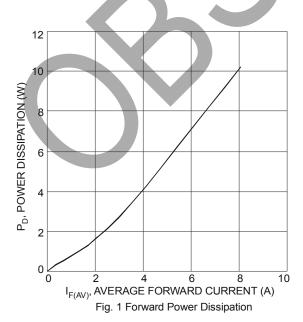
Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resistance Junction to Case (Note 3) Thermal Resistance Junction to Ambient (Note 3)	R _θ JC R _θ JA	10 47	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +175	°C

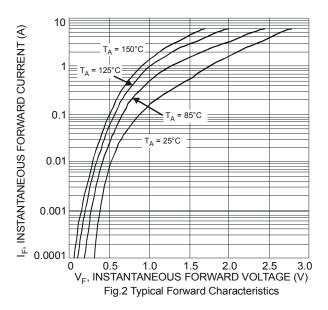
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	VF	-	-	3.0	V	I _F = 6A, T _J = 25°C
Leakage Current (Note 4)	l _R	-	-	50	μA	V _R = 600V, T _J = 25°C
		-	19	23		I _F = 0.5A, I _R = 1A, I _{RR} = 0.25A
Reverse Recovery Time	trr	-	28	35	ns	$I_{F} = 1A, V_{R} = 30V,$
						di/dt = 50A/μs
Softness Factor	S	-	0.3	-	-	I _F = 6A, dl/dt = 200A/μs, V _R = 400V, T _J = 125°C
Reverse Recovery Current	I _{RM}	-	3.6	-	А	
Reverse Recovery Charges	Q _{rr}	-	135	-	nC	
Junction Capacitance	CJ	-	30	-	pF	4.0V, 1MHz

Notes:

Device mounted on Polymide substrate, 1" * 1", 2oz, copper, double-sided, PC boards.
 Short duration pulse test used to minimize self-heating effect.

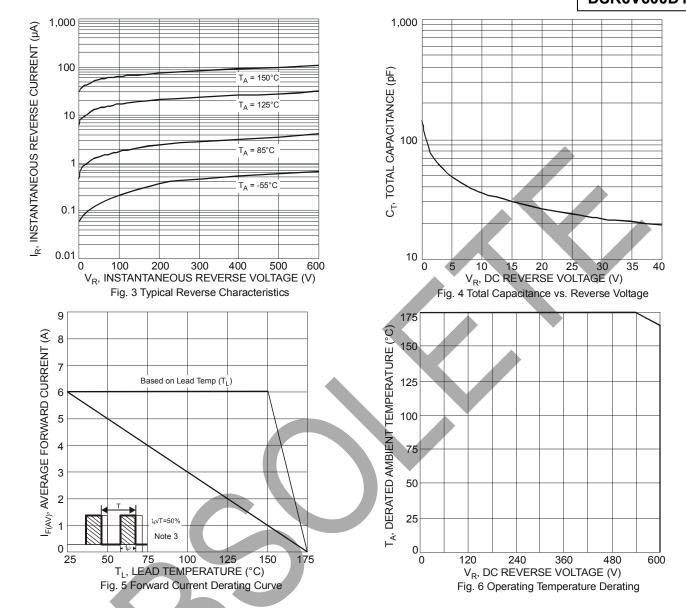




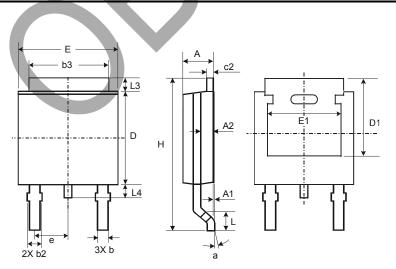


PART OBSOLETE





Package Outline Dimensions



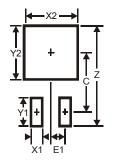
TO252-3L					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	_	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All Dimensions in mm					

DSR6V600D1 Document number: DS33404 Rev. 4 - 4





Suggested Pad Layout



Dimensions	Value (in mm)	
Z	11.6	
X1	1.5	
X2	7.0	
Y1	2.5	
Y2	7.0	
С	6.9	
E1	2.3	

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