

NPN Silicon High-Frequency Transistors

- High Gain–Bandwidth Product
 $f_T = 8.0 \text{ GHz (Typ) @ } 50 \text{ mA}$
- Low Noise Figure
 $NF_{\min} = 1.6 \text{ dB (Typ) @ } f = 1.0 \text{ GHz}$

**$I_C = 80 \text{ mA}$
LOW NOISE
HIGH-FREQUENCY
TRANSISTORS**

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CEO}	10	Vdc
Collector–Base Voltage	V_{CBO}	20	Vdc
Emitter–Base Voltage	V_{EBO}	3.0	Vdc
Collector Current — Continuous	I_C	80	mA
Total Device Dissipation (1) @ $T_C = 75^\circ\text{C}$ Derate above 75°C	P_D	0.58 7.73	Watts mW/°C
Operating and Storage Temperature	T_{stg}	–55 to +150	°C



MRF571

NPN Silicon High-Frequency Transistors

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ($I_C = 1.0\text{ mA}$, $I_B = 0$)	$V_{(BR)CEO}$	10	12	—	Vdc
Collector–Base Breakdown Voltage ($I_C = 0.1\text{ mA}$, $I_E = 0$)	$V_{(BR)CBO}$	20	—	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = 50\text{ }\mu\text{Adc}$, $I_C = 0$)	$V_{(BR)EBO}$	2.5	—	—	Vdc
Collector Cutoff Current ($V_{CB} = 8.0\text{ Vdc}$, $I_E = 0$)	I_{CBO}	—	—	10	μAdc

ON CHARACTERISTICS

DC Current Gain ($I_C = 30\text{ mAdc}$, $V_{CE} = 5.0\text{ Vdc}$)	h_{FE}	50	—	300	—
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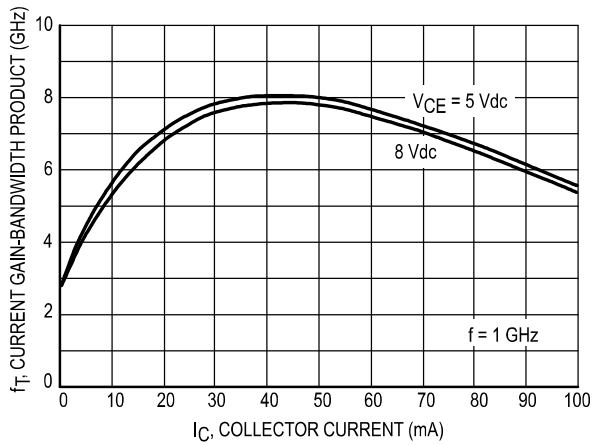
DYNAMIC CHARACTERISTICS

Collector–Base Capacitance ($V_{CB} = 6.0\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	C_{cb}	—	0.75	1.0	pF
Current Gain–Bandwidth Product ($V_{CE} = 8.0\text{ Vdc}$, $I_C = 50\text{ mAdc}$, $f = 1.0\text{ GHz}$)	f_T	—	8.0	—	GHz

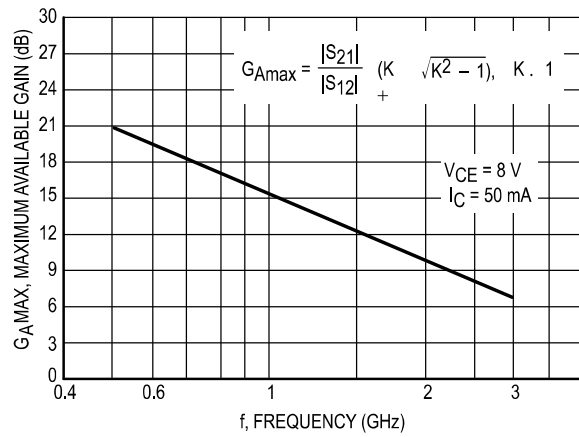
FUNCTIONAL TESTS

Gain @ Noise Figure ($I_C = 10\text{ mAdc}$, $V_{CE} = 6.0\text{ Vdc}$)	MRF571	$f = 0.5\text{ GHz}$	G_{NF}	—	16.5	—	dB
	MRF571	$f = 1.0\text{ GHz}$		10	12	—	
Noise Figure ($I_C = 10\text{ mAdc}$, $V_{CE} = 6.0\text{ Vdc}$)	MRF571	$f = 0.5\text{ GHz}$	NF	—	1.0	—	dB
	MRF571	$f = 1.0\text{ GHz}$		—	1.5	2.0	
	MRF571	$f = 2.0\text{ GHz}$		—	2.8	—	

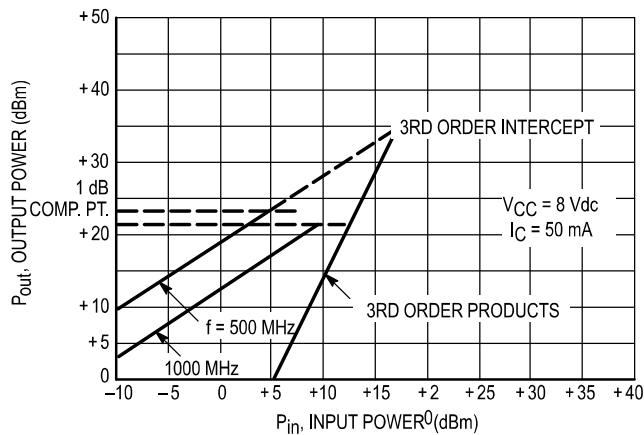
TYPICAL CHARACTERISTICS MRF571



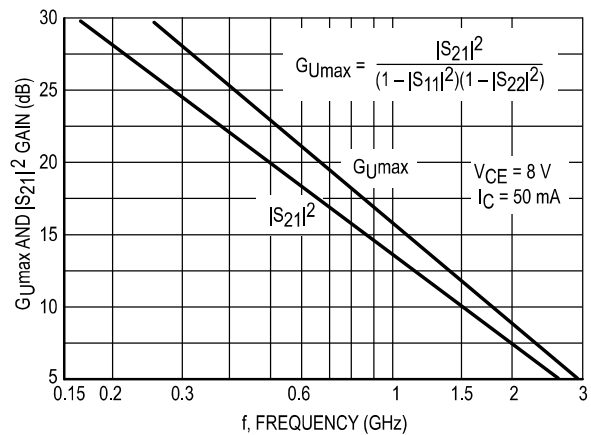
f_T, Current Gain–Bandwidth Product versus Collector Current



G_{Amax}, Maximum Available Gain versus Frequency



1.0 dB Compression Point and Third Order Intercept

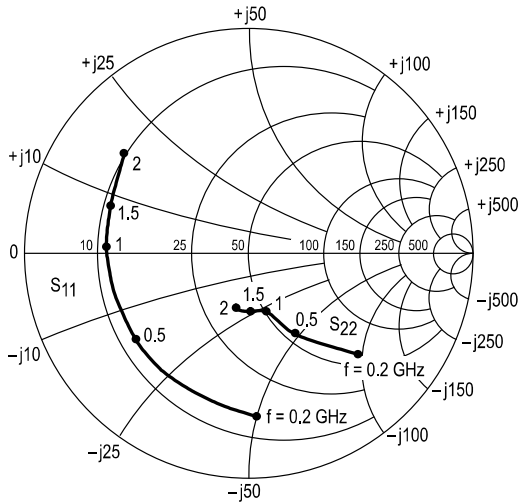


G_{Umax} and |S₂₁|² versus Frequency

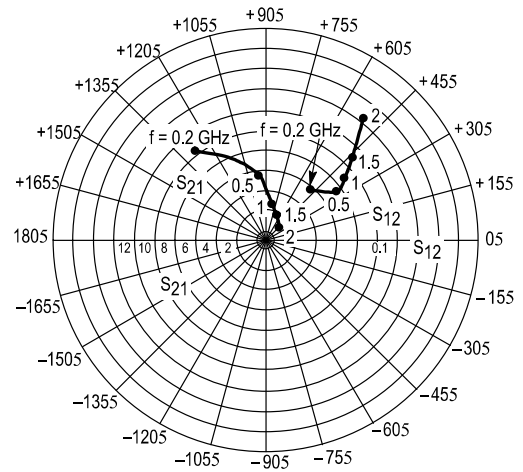
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**Input/Output Reflection Coefficients
 versus Frequency (GHz)**
VCE = 6.0 V, IC = 5.0 mA

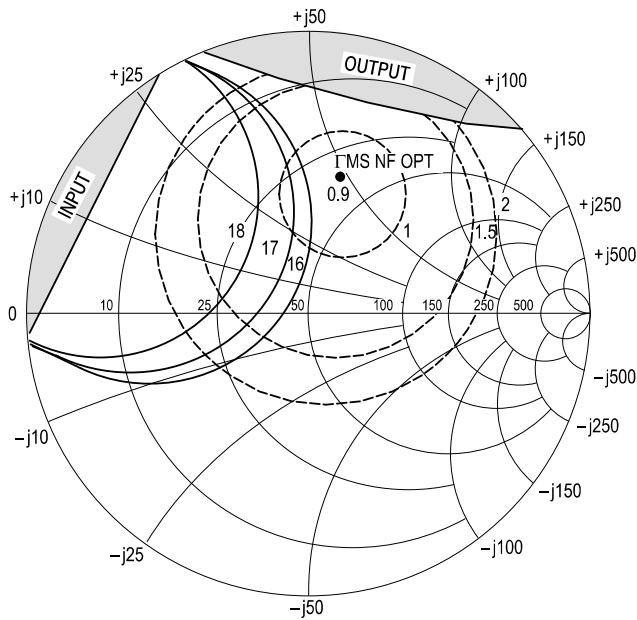


**Forward/Reverse Transmission
 Coefficients versus Frequency (GHz)**
VCE = 6.0 V, IC = 5.0 mA

VCE (Volts)	IC (mA)	f (MHz)	S11		S21		S12		S22	
			S11	- φ	S21	- φ	S12	- φ	S22	- φ
6.0	5	200	0.74	-86	10.5	129	0.06	48	0.69	-42
		500	0.62	-143	5.5	97	0.08	33	0.41	-59
		1000	0.61	178	3.0	78	0.09	37	0.28	-69
		1500	0.65	158	2.0	62	0.11	44	0.26	-88
		2000	0.70	140	1.6	51	0.14	51	0.27	-99
	10	200	0.64	-111	15	118	0.04	44	0.53	-59
		500	0.58	-160	6.9	93	0.06	42	0.27	-77
		1000	0.59	168	3.7	77	0.09	52	0.16	-91
		1500	0.63	151	2.5	64	0.12	56	0.16	-113
		2000	0.67	134	2.0	53	0.16	57	0.16	-118
	50	200	0.56	-160	20.4	102	0.02	57	0.27	-98
		500	0.57	176	8.4	86	0.05	67	0.14	-130
		1000	0.60	156	4.4	75	0.09	70	0.11	-164
		1500	0.62	152	2.9	64	0.13	68	0.13	-175
		2000	0.66	127	2.4	53	0.18	62	0.11	-178
8.0	5	200	0.75	-83	10.7	129	0.06	49	0.71	-39
		500	0.62	-140	5.1	98	0.08	34	0.43	-54
		1000	0.60	-179	3.7	78	0.09	38	0.31	-62
		1500	0.64	159	2.1	62	0.10	45	0.29	-80
		2000	0.69	141	1.7	52	0.13	52	0.29	-91
	10	200	0.64	-99	15.1	120	0.05	46	0.54	-60
		500	0.52	-152	7.1	94	0.07	45	0.32	-75
		1000	0.52	170	3.7	76	0.10	54	0.15	-82
		1500	0.52	150	2.5	62	0.13	56	0.16	-108
		2000	0.57	133	2.0	51	0.18	55	0.16	-107
	50	200	0.52	-153	19.6	102	0.03	56	0.28	-92
		500	0.52	178	8.1	86	0.05	67	0.16	-98
		1000	0.56	157	4.1	73	0.10	70	0.06	-130
		1500	0.54	139	2.8	62	0.13	68	0.11	-146
		2000	0.59	126	2.2	52	0.19	63	0.10	-137

MRF571 Common Emitter S-Parameters

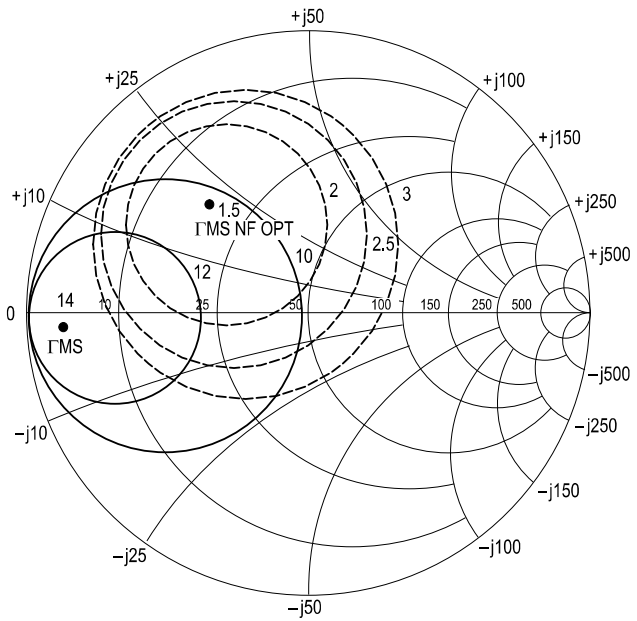




$V_{CE} = 6.0 \text{ V}$, $I_C = 5.0 \text{ mA}$
 $f = 500 \text{ MHz}$
 — REGION OF INSTABILITY

f (GHz)	NF OPT (dB)	Rn (Ω)	NF50 Ω (dB)
0.5	0.9	9.3	1.3

Γ_{MS} NF OPT	K
0.49 – 745	0.58



$V_{CE} = 6.0 \text{ V}$, $I_C = 5.0 \text{ mA}$
 $f = 1.0 \text{ GHz}$

f (GHz)	NF OPT (dB)	Rn (Ω)	NF50 Ω (dB)	Γ_{MS} NF OPT
1.0	1.5	7.5	2.2	0.48 – 1345

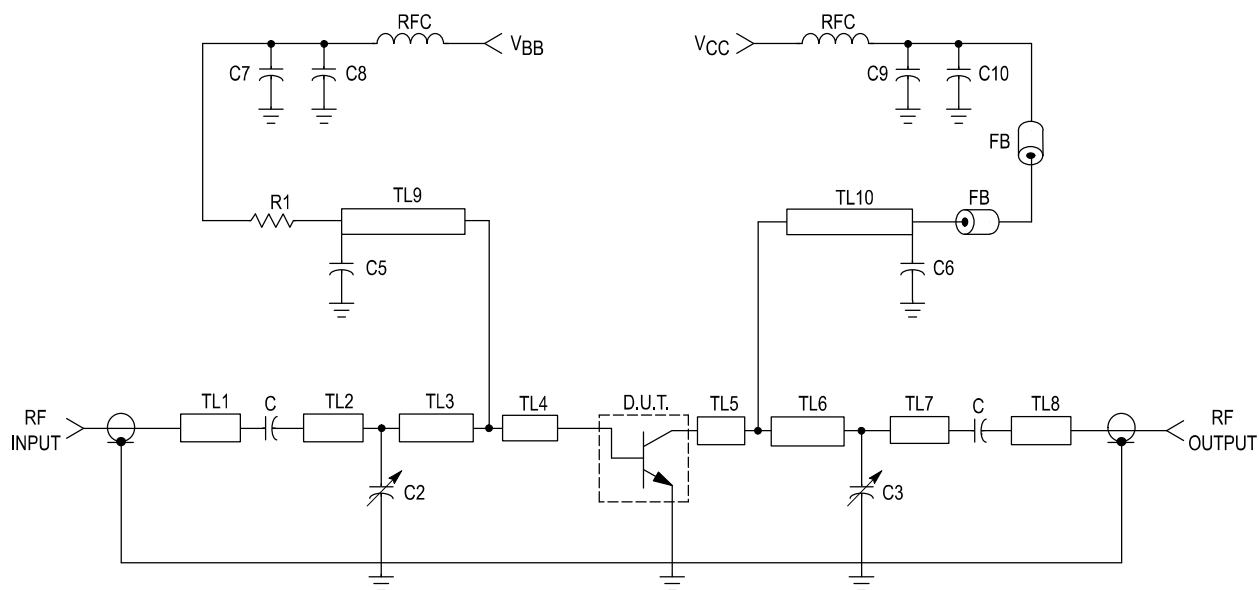
Γ_{MS}	Γ_{ML}
0.89 – -1795	0.81 – 665

MRF571 Constant Gain and Noise Figure Contours



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C1, C4, C5, C6, C8, C9 — 100 pF Chip Capacitor
 C2, C3 — 0.8–8.0 pF Johanson Capacitor
 C7, C10 — 10 μ F Tantalum Capacitor
 R1 — 1.0 kOhms Res.
 RFC — VK-200, Ferroxcube
 FB — Ferrite Bead, Ferroxcube 56-590-65/3B
 Board Material — 0.0625, Glass Teflon, $\epsilon_r = 2.55$

TL1, TL7, TL8 — Microstrip 0.162, x 0.600,
 TL2 — Microstrip 0.162, x 1.060,
 TL3 — Microstrip 0.162, x 0.700,
 TL4, TL5 — Microstrip 0.162, x 0.440,
 TL6 — Microstrip 0.162, x 1.140,
 TL8, TL9 — Microstrip 0.020, x 2.130,

MRF571 Test Circuit Schematic

PACKAGE DIMENSIONS

