

NP20P06YLG

R07DS0706EJ0100

Rev.1.00

Apr 17, 2012

MOS FIELD EFFECT TRANSISTOR

Description

The NP20P06YLG is P-channel MOS Field Effect Transistor designed for high current switching applications.

Features

- Low on-state resistance
 $R_{DS(on)} = 47 \text{ m}\Omega \text{ MAX. (} V_{GS} = -10 \text{ V, } I_D = -10 \text{ A)}$
 $R_{DS(on)} = 64 \text{ m}\Omega \text{ MAX. (} V_{GS} = -5 \text{ V, } I_D = -10 \text{ A)}$
 $R_{DS(on)} = 70 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.5 \text{ V, } I_D = -10 \text{ A)}$
- Logic level drive type
- Gate to Source ESD protection diode built in
- Designed for automotive application and AEC-Q101 qualified

Ordering Information

Part No.	Lead Plating	Packing		Package
NP20P06YLG-E1-AY *1	Pure Sn (Tin)	Tape 2500 p/reel	Taping (E1 type)	8-pin HSON
NP20P06YLG-E2-AY *1			Taping (E2 type)	

Note: *1 Pb-free (This product does not contain Pb in the external electrode)

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to Source Voltage ($V_{GS} = 0 \text{ V}$)	V_{DSS}	-60	V
Gate to Source Voltage ($V_{DS} = 0 \text{ V}$)	V_{GSS}	∓ 20	V
Drain Current (DC) ($T_C = 25^\circ\text{C}$)	$I_{D(DC)}$	∓ 20	A
Drain Current (pulse) *1	$I_{D(pulse)}$	∓ 60	A
Total Power Dissipation ($T_C = 25^\circ\text{C}$)	P_{T1}	57	W
Total Power Dissipation ($T_A = 25^\circ\text{C}$) *2	P_{T2}	1.0	W
Channel Temperature	T_{ch}	175	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +175	$^\circ\text{C}$
Single Avalanche Current *3	I_{AS}	17	A
Single Avalanche Energy *3	E_{AS}	29	mJ

Thermal Resistance

Channel to Case Thermal Resistance $R_{th(ch-C)}$ 2.63 $^\circ\text{C/W}$

Channel to Ambient Thermal Resistance *2 $R_{th(ch-A)}$ 150 $^\circ\text{C/W}$

Notes: *1 $T_C = 25^\circ\text{C}$, $P_W \leq 10 \mu\text{s}$, Duty Cycle $\leq 1\%$

*2 Mounted on glass epoxy substrate of 40 mm \times 40 mm \times 1.6 mm with 4% copper area (35 μm)

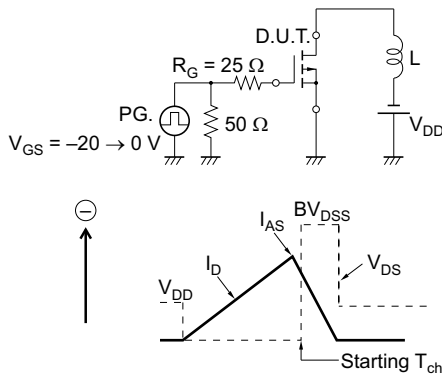
*3 $T_{ch(start)} = 25^\circ\text{C}$, $V_{DD} = -30 \text{ V}$, $R_G = 25 \Omega$, $L = 100 \mu\text{H}$, $V_{GS} = -20 \text{ V} \rightarrow 0 \text{ V}$

Electrical Characteristics (T_A = 25°C)

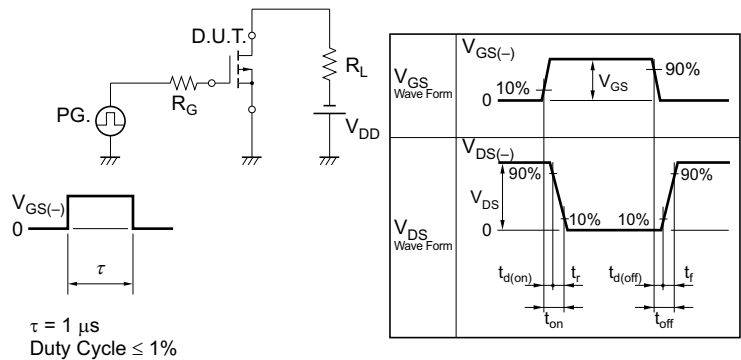
Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -60 V, V _{GS} = 0 V
Gate Leakage Current	I _{GSS}	—	—	±10	μA	V _{GS} = ±20 V, V _{DS} = 0 V
Gate to Source Threshold Voltage	V _{GS(th)}	-1.0	-1.7	-2.5	V	V _{DS} = V _{GS} , I _D = -250 μA
Forward Transfer Admittance *1	y _{fs}	9	20	—	S	V _{DS} = -5 V, I _D = -10 A
Drain to Source On-state Resistance *1	R _{DS(on)1}	—	37	47	mΩ	V _{GS} = -10 V, I _D = -10 A
	R _{DS(on)2}	—	41	64	mΩ	V _{GS} = -5 V, I _D = -10 A
	R _{DS(on)3}	—	43	70	mΩ	V _{GS} = -4.5 V, I _D = -10 A
Input Capacitance	C _{iss}	—	1605	2407	pF	V _{DS} = -25 V
Output Capacitance	C _{oss}	—	150	225	pF	V _{GS} = 0 V
Reverse Transfer Capacitance	C _{rss}	—	96	173	pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}	—	8	16	ns	V _{DD} = -30 V, I _D = -10 A V _{GS} = -10 V R _G = 0 Ω
Rise Time	t _r	—	8	20	ns	
Turn-off Delay Time	t _{d(off)}	—	160	320	ns	
Fall Time	t _f	—	80	200	ns	
Total Gate Charge	Q _G	—	34	51	nC	V _{DD} = -48 V V _{GS} = -10 V I _D = -20 A
Gate to Source Charge	Q _{GS}	—	4	—	nC	
Gate to Drain Charge	Q _{GD}	—	9	—	nC	
Body Diode Forward Voltage *1	V _{F(S-D)}	—	0.95	1.5	V	I _F = 20 A, V _{GS} = 0 V
Reverse Recovery Time	t _{rr}	—	38	—	ns	I _F = 20 A, V _{GS} = 0 V
Reverse Recovery Charge	Q _{rr}	—	50	—	nC	di/dt = 100 A/μs

Note: *1 Pulsed test

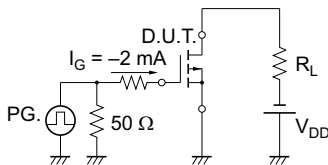
TEST CIRCUIT 1 AVALANCHE CAPABILITY



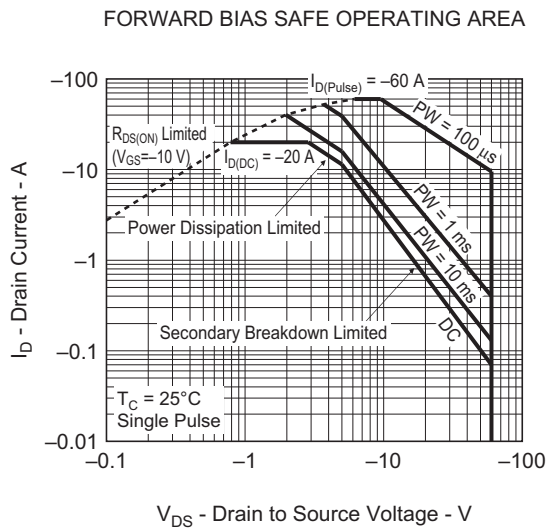
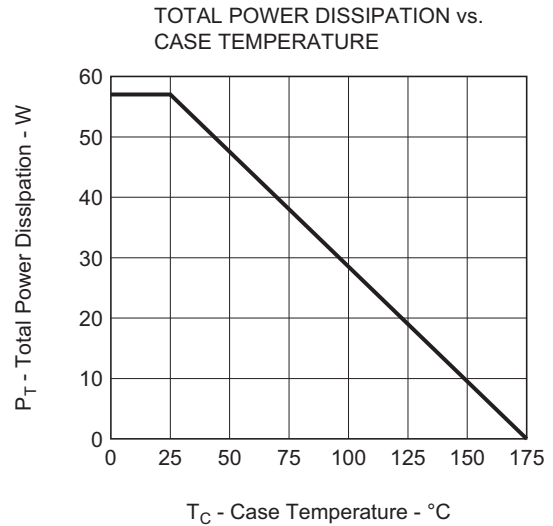
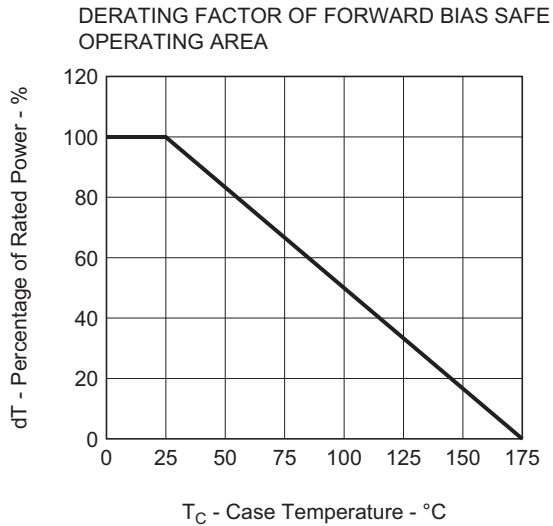
TEST CIRCUIT 2 SWITCHING TIME



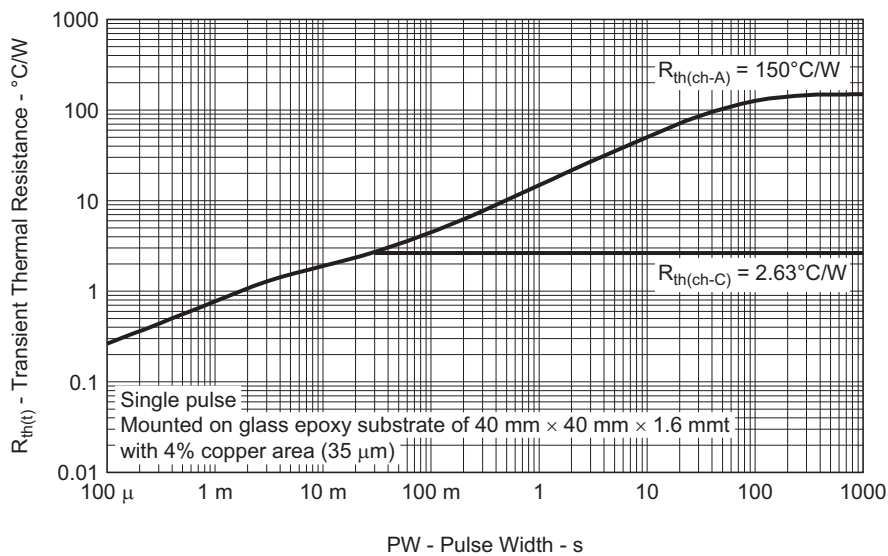
TEST CIRCUIT 3 GATE CHARGE



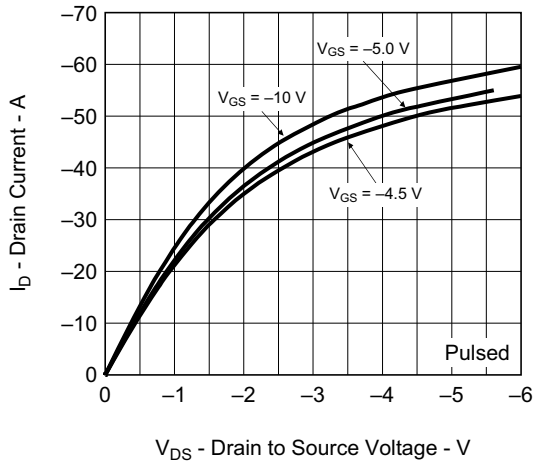
Typical Characteristics (T_A = 25°C)



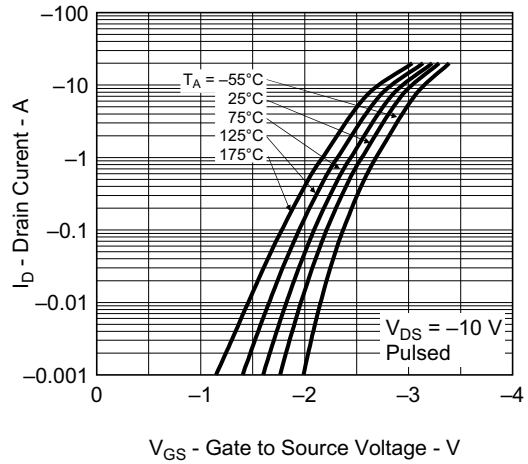
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



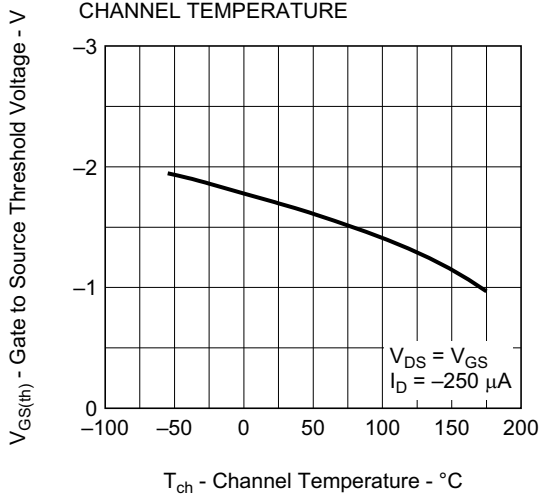
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



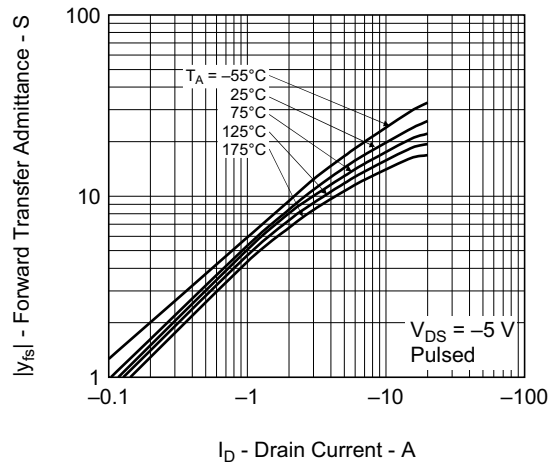
FORWARD TRANSFER CHARACTERISTICS



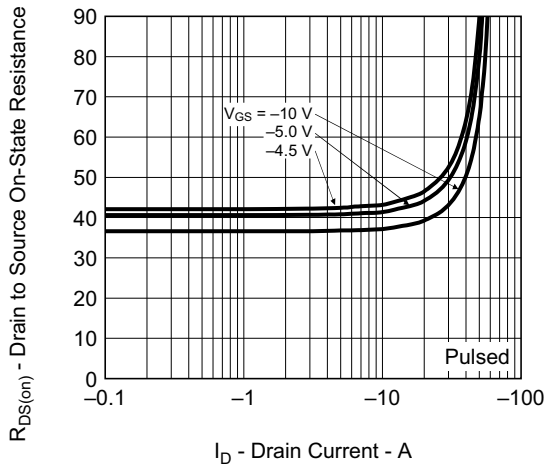
GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE



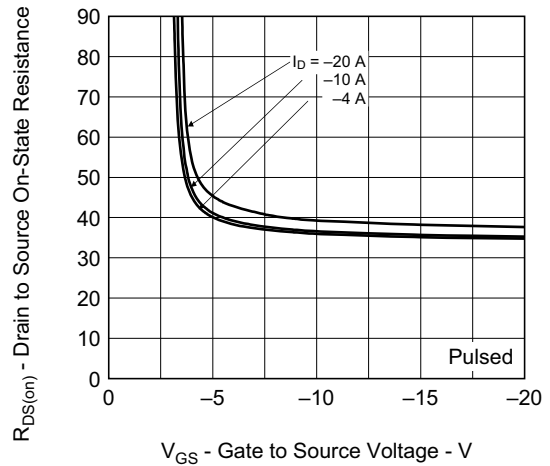
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



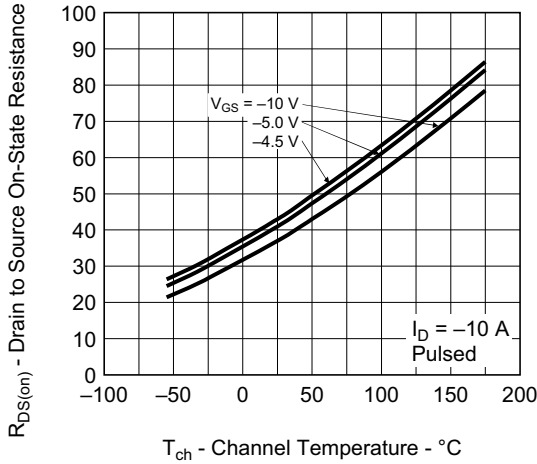
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



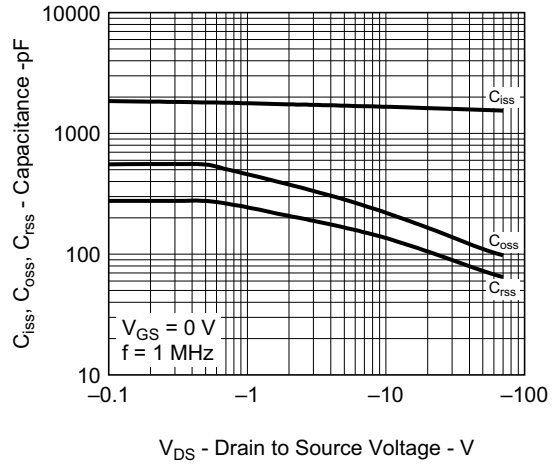
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



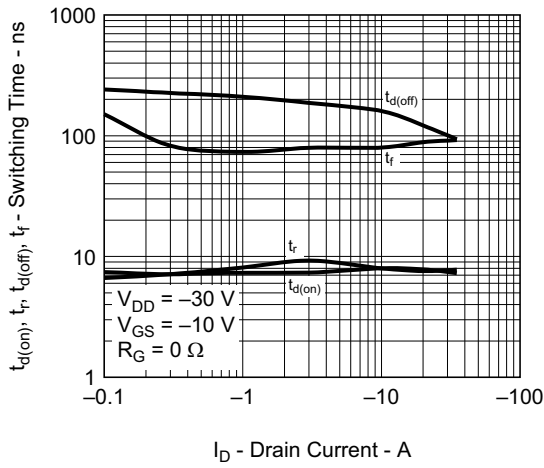
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



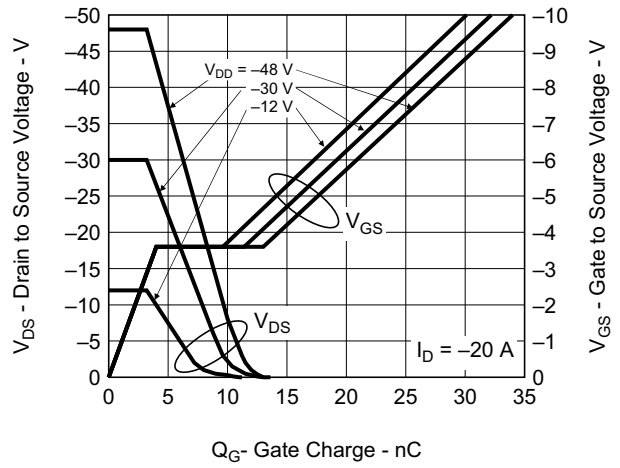
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



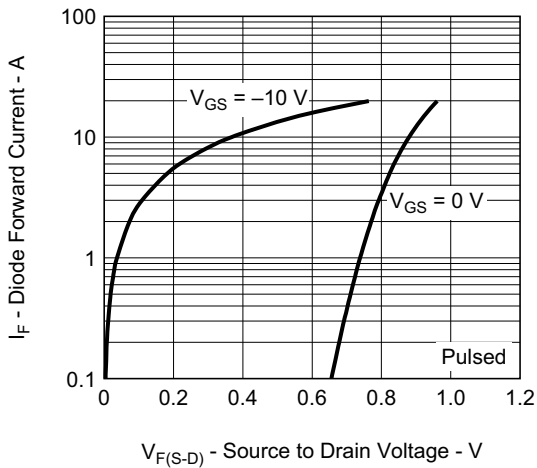
SWITCHING CHARACTERISTICS



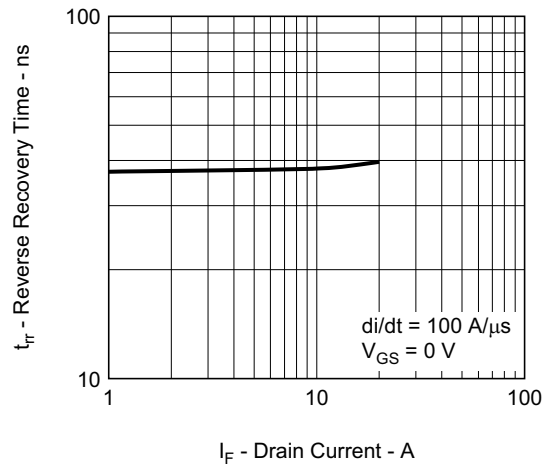
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE

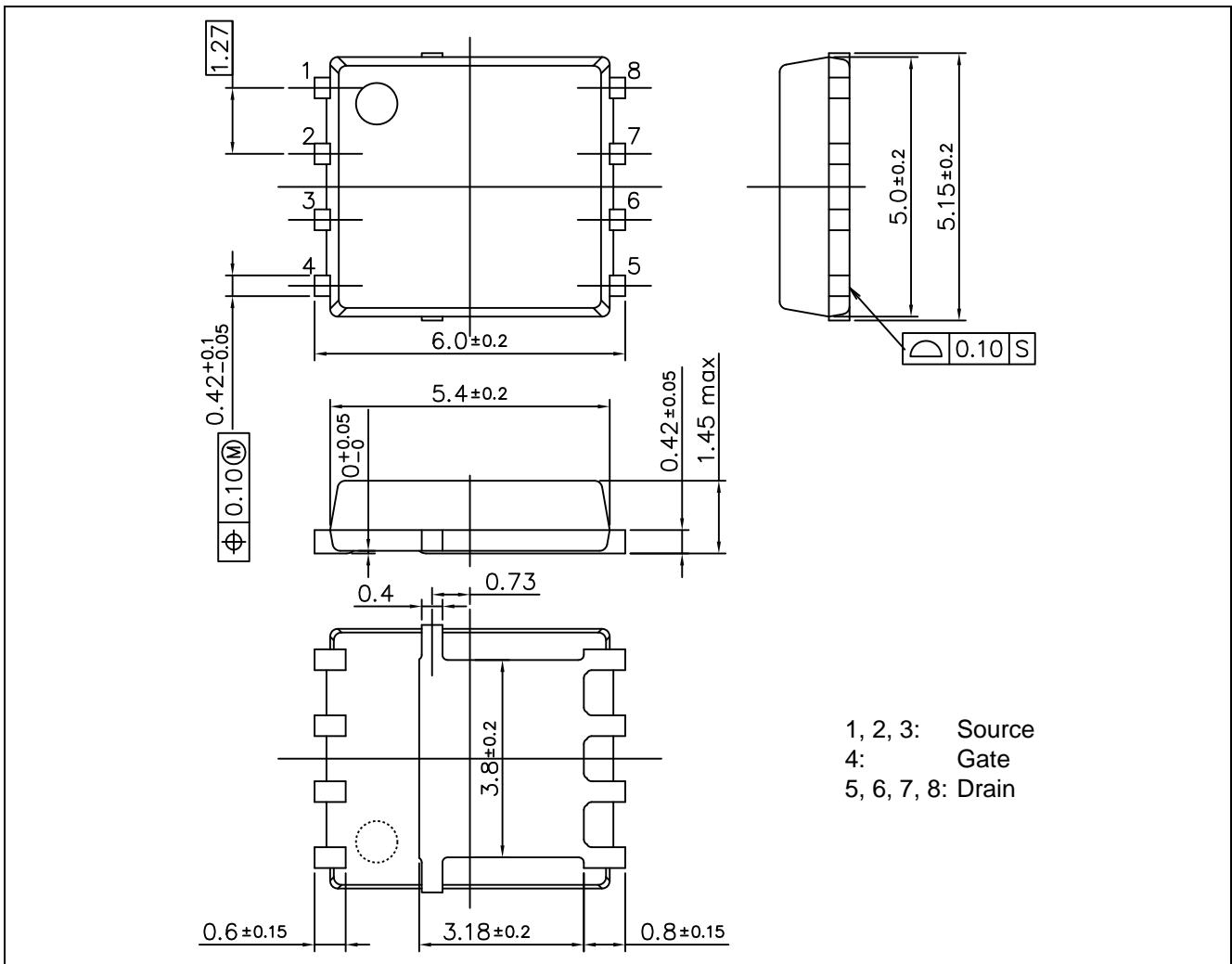


REVERSE RECOVERY TIME vs. DRAIN CURRENT

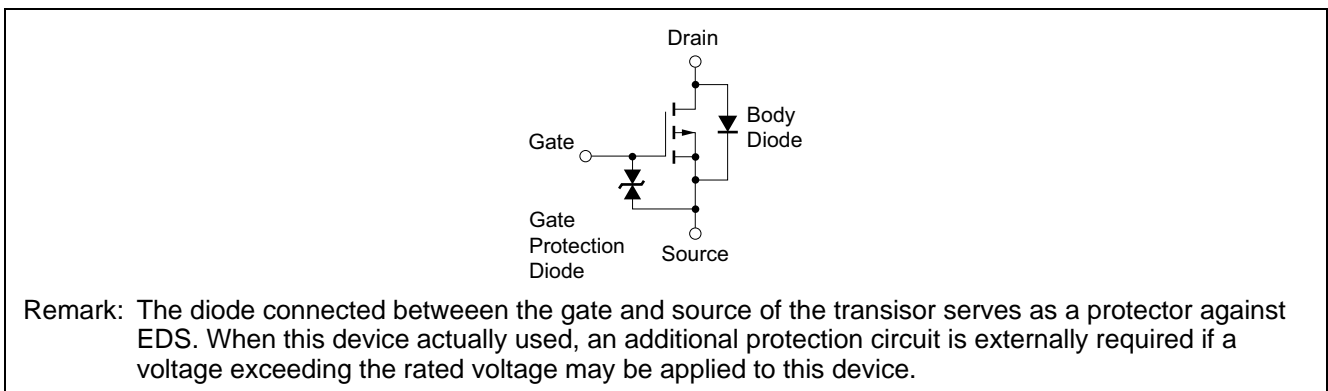


Package Drawings (Unit: mm)

8-pin HSON (Mass: 0.13 g TYP.)



Equivalent Circuit



Revision History	NP20P06YLG Data Sheet
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Rev.	Date	Description	
		Page	Summary
1.00	Apr 17, 2012	—	First Edition Issued

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