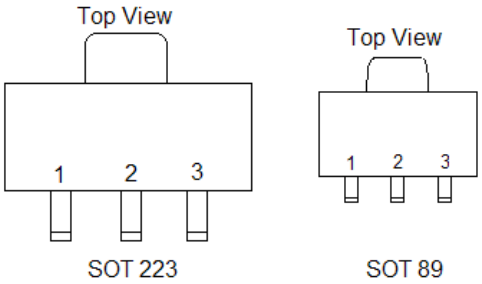
Description

The SE8119 series of high performance low dropout voltage regulators are designed for applications that require efficient conversion and fast transient response.

Features

- Low Dropout Performance.
- Guaranteed 800mA Output Current.
- Wide Input Supply Voltage Range.
- Over-temperature and Over-current Protection.
- Rugged 3KV ESD withstand capability.
- Available in SOT-89-3L and SOT-223-3L Packages.

Pin Configuration

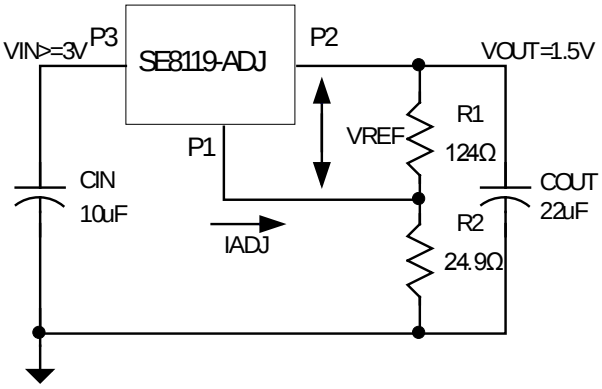


Application

- PC-Camera
- Active SCSI Terminators.
- High Efficiency Linear Regulators.
- 5V to 3.3V Linear Regulators
- Motherboard Clock Supplies.

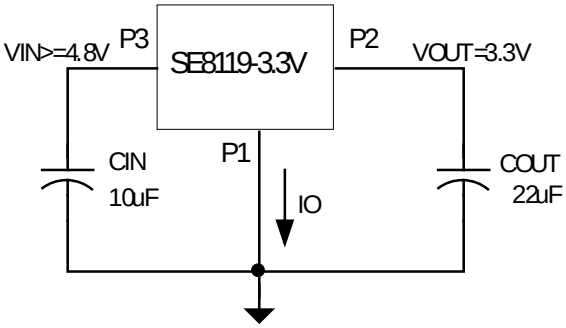
Typical Application

Adjustable Voltage Regulator



$$V_{OUT}=V_{REF}(1+R_2/R_1)+I_{ADJ}R_2$$

Fixed Voltage Regulator



Ordering/Marking Information

Device	Marking Information	Package	Information
SE8119AKXLF	SE8119AKXLF/HF	SOT-89	A denotes 3.3V, K denotes SOT-89, X denotes pin options (G:OUT/GND/IN N: GND/IN/OUT T:GND/OUT/IN)
SE8119ADJLF	SE8119ADJLF/HF		Adjustable output voltage.

Note:LF/HF mean for Lead Free or Halogen Free ,Marking for special order

Ordering/Marking Information

Device	Marking Information	Package	V_{OUT}
SE8119TXXLF	SE8119TXXLF/HF	SOT-223	Fixed output voltages; X denotes voltage options (1.5V,1.8V, 2.5V,3.3V and 5.0V).
SE8119TALF	SE8119TALF/HF		Adjustable output voltage.

Absolute Maximum Rating

Symbol	Parameter	Maximum	Units
V_{IN}	Input Supply Voltage	9	V
T_J	Operating Junction Temperature Range	0 to 125	°C
T_{STG}	Storage Temperature Range	-40 to 150	°C
T_{LEAD}	Lead Temperature (Soldering 10 Sec)	260	°C

Electrical Characteristic

$V_{IN,MAX} \leq 8V$, $V_{IN,MIN} - V_{OUT} = 1.5V$, $I_{OUT} = 10mA$, $C_{IN} = 10\mu F$, $C_{OUT} = 22\mu F$, $T_J = 0 – 125°C$, unless otherwise specified.

Symbol	Parameter	Test Condition	Min	Typ	Max	Units
V_O	Output Voltage	SE8119-3.3	3.234	3.3	3.366	V
V_{REF}	Reference Voltage (Adj. Voltage Version)	$(V_{IN} - V_{OUT}) = 1.5V$ $I_{OUT} = 10mA$	(-2%)	1.250	(+2%)	V
V_{SR}	Line Regulation	$V_{OUT} + 1.5V < V_{IN} < 8V$ $I_{OUT} = 10mA$	--	0.3	--	%
V_{LR}	Load Regulation (1)	$(V_{IN} - V_{OUT}) = 1.5V$ $10mA \leq I_{OUT} \leq 800mA$	--	1.2	--	%
I_Q	Quiescent Current		--	2.6	--	mA
I_{ADJ}	Adjust Pin Current		--	51	--	μA
ΔI_{ADJ}	Adjust Pin Current Change	$V_{OUT} + 1.5V < V_{IN} < 8V$ $10mA \leq I_{OUT} \leq 800mA$	--	6	--	μA
V_D	Dropout Voltage (1), (2)	$I_{OUT} = 800mA$	--	1.5	--	V
I_O	Minimum Load Current		--	0.4	--	mA
VI_{CL}	Current Limit (1)		--	0.9	--	A
T_C	Temperature Coefficient		--	0.05	--	%/°C
OTP	Thermal Protection		--	150	--	°C
V_N	RMS Output Noise	$T_A = 25°C$, $10Hz \leq f \leq 10kHz$	--	0.003	--	$\%V_O$
R_A	Ripple Rejection Ratio	f = 120Hz, $C_{OUT} = 22\mu F$ (Tantalum), $(V_{IN} - V_{OUT}) = 2V$, $I_{OUT} = 10mA$	--	58	--	dB

Notes:

1. Low duty cycle pulse testing with which T_J remains unchanged.
2. $\Delta V_{OUT} = 1\%$.

Application Hints

Like any linear voltage regulator, SE8119 requires external capacitors to ensure stability. The external capacitors must be carefully selected to ensure performance.

Input Capacitor

An input capacitor of at least 10μF is required. Ceramic or Tantalum can be used. The value can be increased without upper limit.

Output Capacitor

An output capacitor is required for stability. It must be placed no more than 1 cm away from the V_{OUT} pin, and connected directly between V_{OUT} and GND pins. The minimum value is 22μF but may be increased without limit.

Thermal Considerations

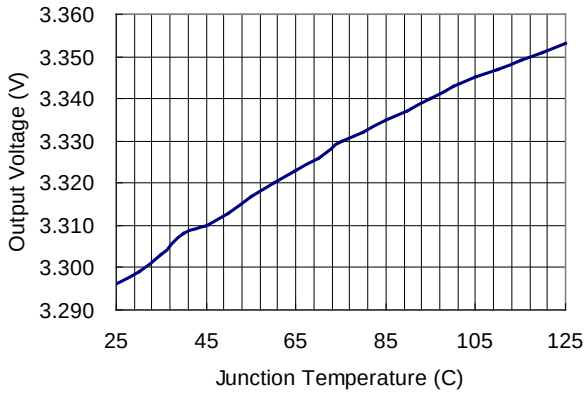
It is important that the thermal limit of the package is not exceeded. The SE8119 has built-in thermal protection. When the thermal limit is exceeded, the IC will enter protection, and V_{OUT} will be pulled to ground. The power dissipation for a given application can be calculated as following:

The power dissipation (P_D) is

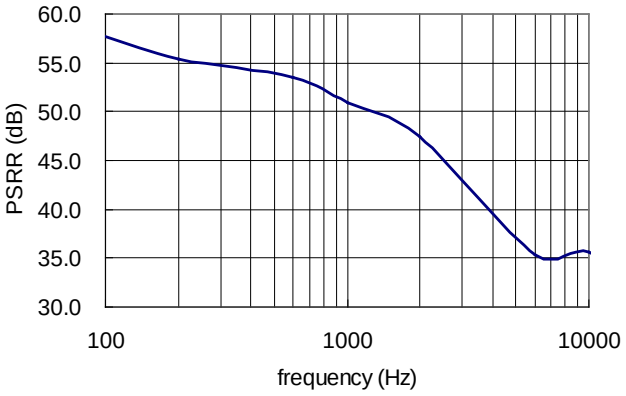
$P_D = I_{OUT} * [V_{IN} – V_{OUT}]$

The thermal limit of the package is then limited to $P_{D(MAX)} = [T_J – T_A]/\Theta_{JA}$ where T_J is the junction temperature, TA is the ambient temperature, and Θ_{JA} is around 150°C/W for SE8119. SE8119 is designed to enter thermal protection at 150°C. For example, if T_A is 25°C then the maximum P_D is limited to about 1.0W. In other words, if I_{OUT} = 500mA, then $[V_{IN} – V_{OUT}]$ can not exceed 2V.

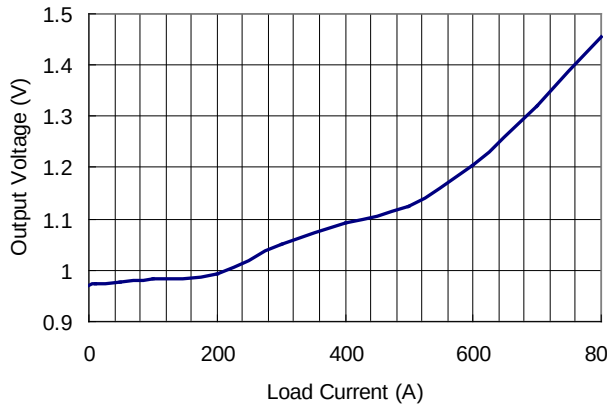
Output Voltage vs Junction Temperature



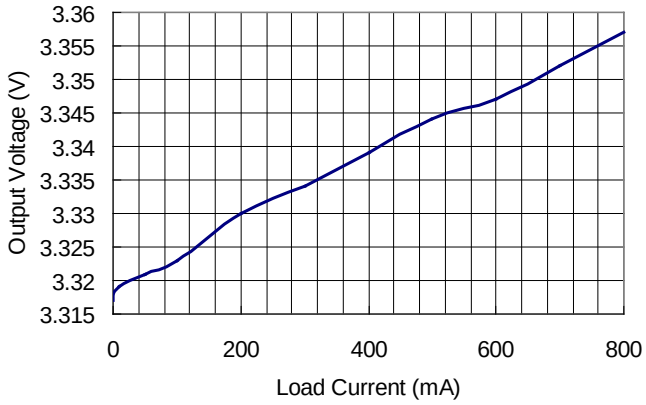
PSRR vs Frequency



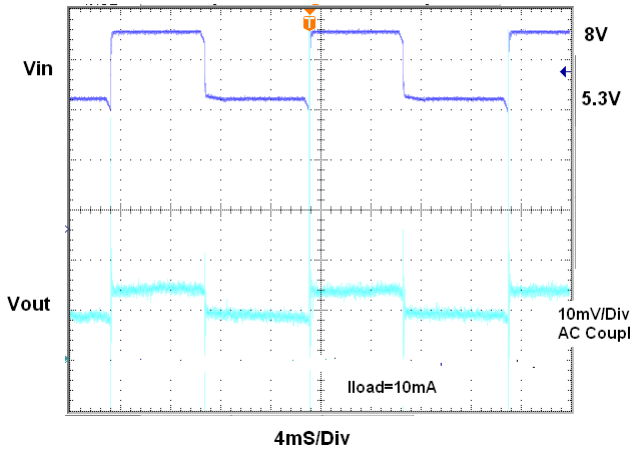
Dropout Voltage vs Load Current



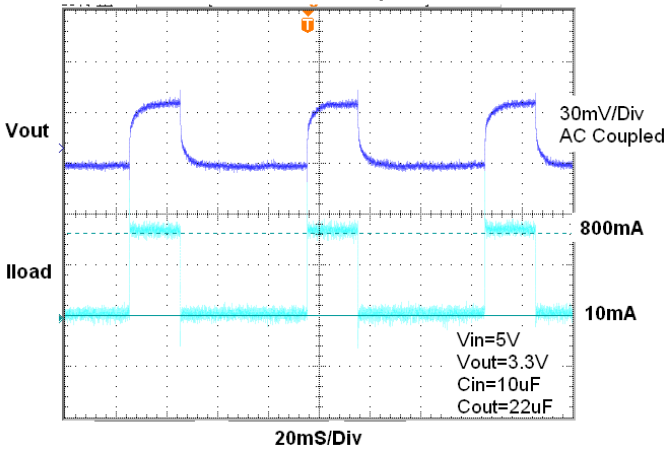
Output Voltage vs Load Current



Line Transient Response

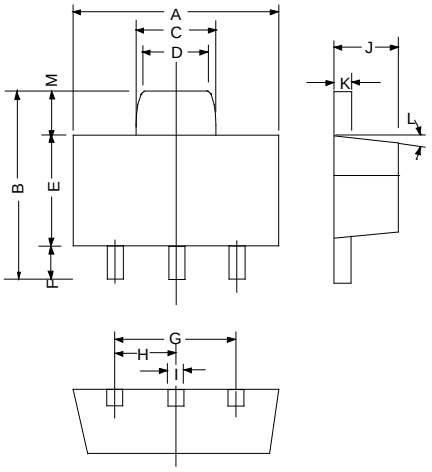


Load Transient Response



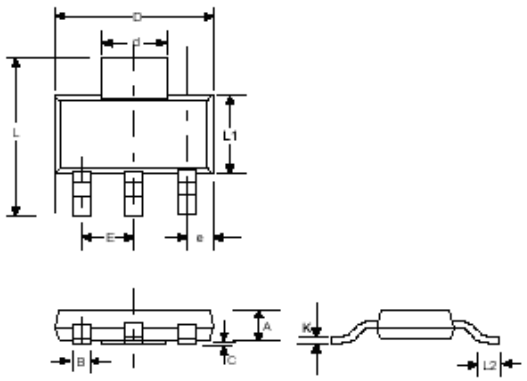
Outline Drawing for SOT-89-3L

SOT-89



DIMENSIONS				
DIM[N]	INCHES		MM	
	MIN	MAX	MIN	MAX
A	0.173	0.181	4.400	4.600
B	0.159	0.167	4.050	4.250
C	0.067	0.075	1.700	1.900
D	0.051	0.059	1.300	1.500
E	0.094	0.102	2.400	2.600
F	0.035	0.047	0.890	1.200
G	0.118REF		3.00REF	
H	0.059REF		1.50REF	
I	0.016	0.020	0.400	0.520
J	0.055	0.063	1.400	1.600
K	0.014	0.016	0.350	0.410
L	10°TYP		10°TYP	
M	0.028REF		0.70REF	

Outline Drawing for SOT-223



DIMENSIONS				
DIM[N]	INCHES		MM	
	MIN	MAX	MIN	MAX
A	—	0.071	—	1.80
B	0.025	0.033	0.640	0.840
C	0.012	—	0.31	—
D	0.248	0.264	6.30	6.71
d	0.115	0.124	2.95	3.15
E	—	0.090	—	2.29
e	0.033	0.041	0.840	1.04
L	0.264	0.287	6.71	7.29
L1	0.130	0.148	3.30	3.71
L2	0.012	—	0.310	—
K	0.010	0.014	0.250	0.360

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