

## 900V N-Channel MOSFET

### **Description**

The MSF9N90 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The TO-220F package is universally preferred for all commercial-industrial applications

#### **Features**

- RDS(on) (Max 1.4 Ω )@VGS=10V
- Gate Charge (Typical 47nC)
- · Improved dv/dt Capability, High Ruggedness
- 100% Avalanche Tested
- Maximum Junction Temperature Range (150°C)
- · RoHS compliant package

### **Application**

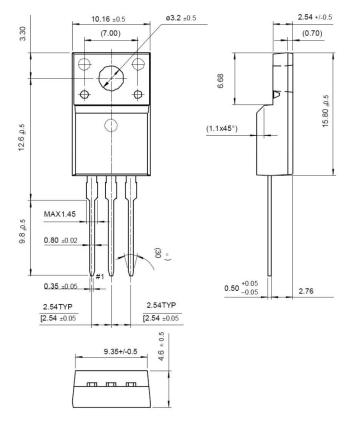
- · Adapter
- · Switching Mode Power Supply

### **Packing & Order Information**

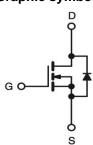
50/Tube; 1,000/Box







### **Graphic symbol**



### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)						
Symbol	Parameter	Value	Unit			
$V_{\text{DSS}}$	Drain-Source Voltage	900	V			
$V_{GS}$	Gate-Source Voltage	±30	V			
I-	Drain Current -Continuous (TC=25°C)	9	А			
I <sub>D</sub>	Drain Current -Continuous (TC=100°C)	6	Α			
$I_{DM}$	Drain Current Pulsed	36	Α			
E <sub>AS</sub>	Single Pulsed Avalanche Energy	900	mJ			
E <sub>AR</sub>	Repetitive Avalanche Energy	28	mJ			
dV/dt	Peak Diode Recovery dV/dt	4.0	V/ns			
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	°C			



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Absolute Maximum Ratings (Tc=25°C unless otherwise noted)					
Symbol	Parameter	Value	Unit		
$P_D$	Power Dissipation (TC = 25 °C)	280	W		
	Power Dissipation (TC = 100 °C)	2.22	W/°C		
TL	Maximum lead temperature for soldering purposes,	200	°C		
	1/8" from case for 5 seconds	300	°C		

• Drain current limited by maximum junction temperature

Thermal Resistance Characteristics						
Symbol	Parameter	Max.	Units			
$R_{\theta J}c$	Junction-to-Case	3.5	°C/W			
$R_{\theta JA}$	Junction-to-Ambient	62.5	C/VV			

On Characteristics						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$V_{GS}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	3.0		5.0	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}$ , $I_D = 4.5 \text{ A}$		1.1	1.4	Ω

Off Characteristics						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu A$	900			V
$\Delta BV_{DSS}$ $/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C		1.05		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 900 V , V <sub>GS</sub> = 0 V V <sub>DS</sub> = 720 V , V <sub>C</sub> = 125°C			10 100	μA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30 V , V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30 V , V <sub>DS</sub> = 0 V			-100	nA

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
C <sub>ISS</sub>	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ F = 1.0 MHz		2200		pF
Coss	Output Capacitance			180		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			15		pF



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Dynamic Characteristics							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
$t_{d(on)}$	Turn-On Time			60		ns	
t <sub>r</sub>	Turn-On Time	$V_{DS} = 450 \text{ V}, I_{D} = 9 \text{ A},$		130		ns	
$t_{\text{d(off)}}$	Turn-Off Delay Time	$R_G = 25 \Omega$		110		ns	
tf	Turn-Off Fall Time			80		ns	
$Q_g$	Total Gate Charge			47		nC	
$Q_{gs}$	Gate-Source Charge	$V_{DS} = 720 \text{ V}, I_{D} = 10 \text{ A},$ $V_{GS} = 9 \text{ V}$		15		nC	
$Q_{gd}$	Gate-Drain Charge	VGS - 9 V		20		nC	

Source-Drain Diode Maximum Ratings and Characteristics							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
Is	Continuous Source-Drain Diode Forwa	tinuous Source-Drain Diode Forward Current 9			9		
I <sub>SM</sub>	Pulsed Source-Drain Diode Forward Current				35	Α	
V <sub>SD</sub>	Source-Drain Diode Forward Voltage	I <sub>S</sub> = 9 A , V <sub>GS</sub> = 0 V			1.4	V	
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> = 9 A , V <sub>GS</sub> = 0 V		550		ns	
Q <sub>rr</sub>	Reverse Recovery Charge	diF/dt = 100A/μs		6.5		μC	

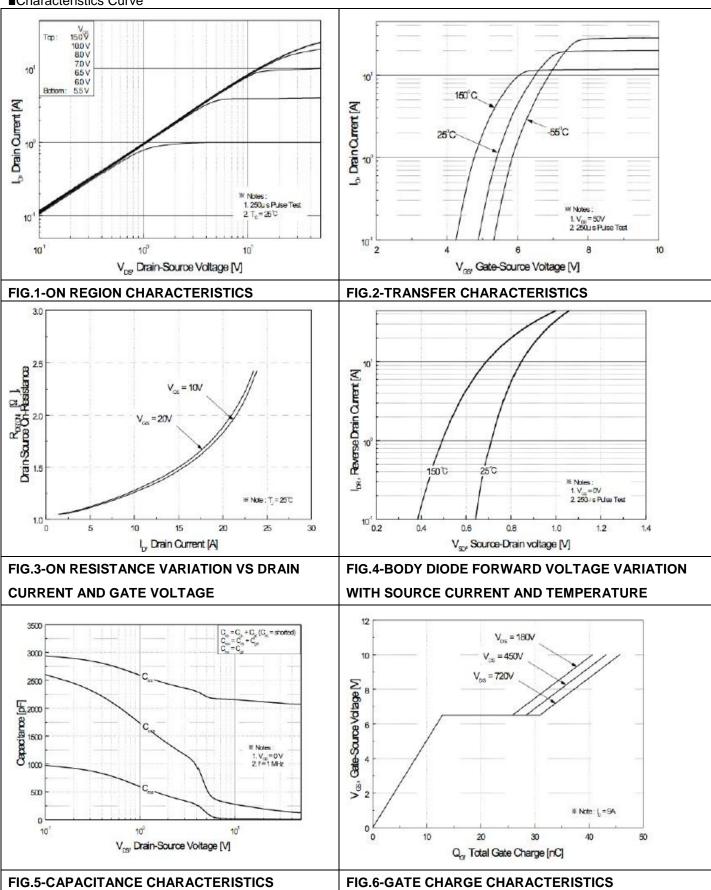
#### Notes;

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=21mH,  $I_{AS}$ =9.0A,  $V_{DD}$ =50V,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C
- 3.  $I_{SD}$   $\leq$  9.0A, di/dt  $\leq$  200A/ $\mu$ s,  $V_{DD}$   $\leq$  BV<sub>DSS</sub>, Starting  $T_J$ =25°C
- 4. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle≤ 2%
- 5. Essentially Independent of Operating Temperature



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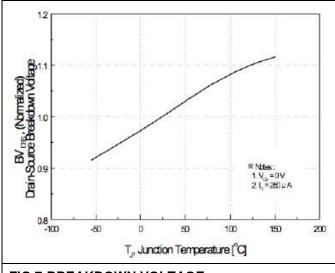
#### ■Characteristics Curve





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### ■Characteristics Curve



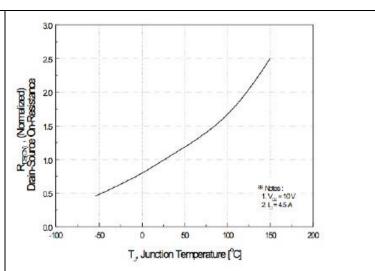


FIG.7-BREAKDOWN VOLTAGE VARIATION VS TEMPERATURE

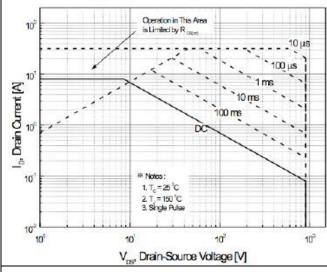


FIG.8-ON-RESISTANCE VARIATION VS TEMPERATURE

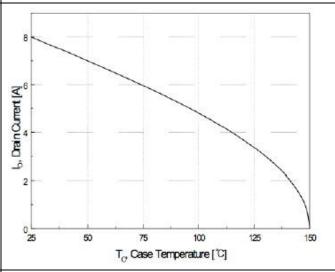


FIG.9-MAXIMUM SAFE OPERATING AREA



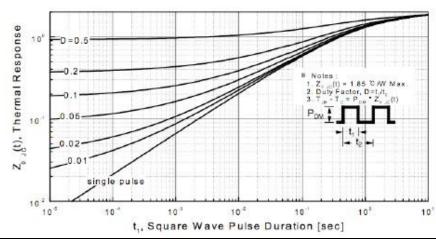


FIG.11-TRANSIENT THERMAL RESPONSE CURVE



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■Characteristics Test Circuit & Waveform

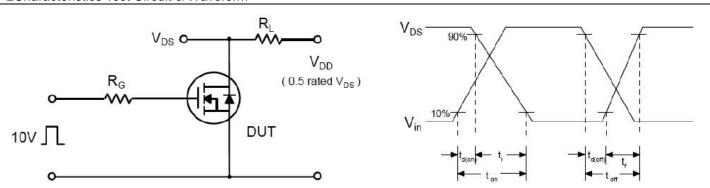


Fig 12. Resistive Switching Test Circuit & Waveforms

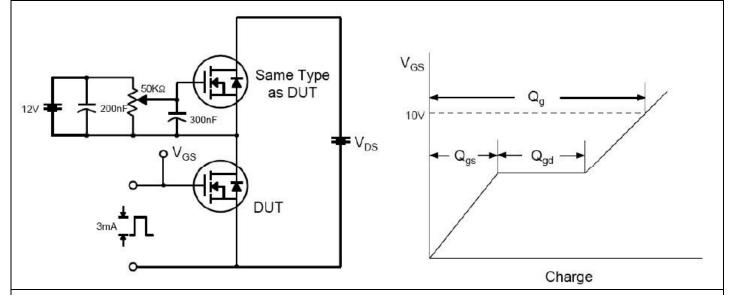
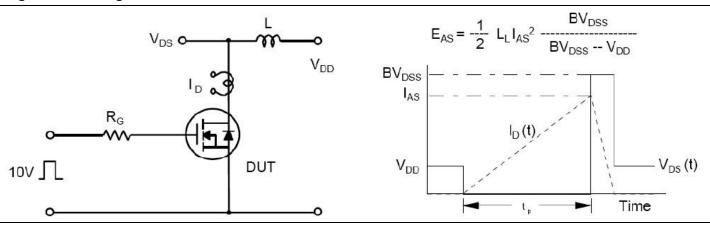


Fig 13. Gate Charge Test Circuit & Waveform





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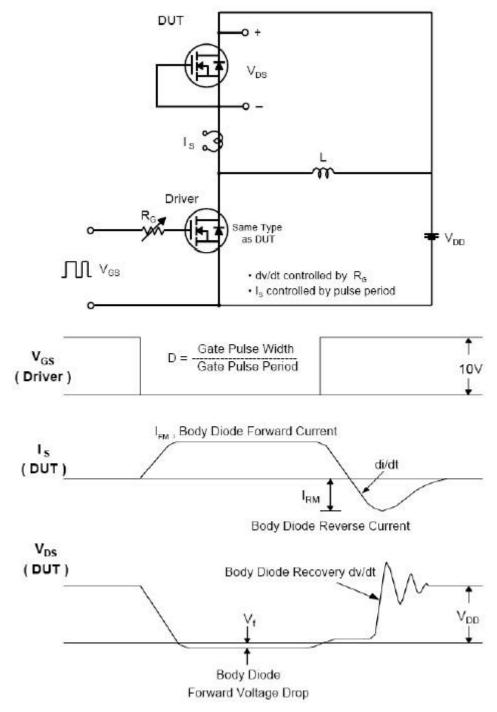


Fig 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



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