Complementary Dual General Purpose Amplifier Transistor

PNP and NPN Surface Mount

Features

- High Voltage and High Current: $V_{CEO} = 50 \text{ V}$, $I_C = 200 \text{ mA}$
- High h_{FE}: h_{FE} = $200 \sim 400$
- Moisture Sensitivity Level: 1
- ESD Rating Human Body Model: 3A
 - Machine Model: C
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	60	Vdc
Collector-Emitter Voltage	V _{(BR)CEO}	50	Vdc
Emitter-Base Voltage	V _{(BR)EBO}	7.0	Vdc
Collector Current - Continuous	Ic	200	mAdc

THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation T _A = 25°C Derate above 25°C	P _D	187 (Note 1) 256 (Note 2) 1.5 (Note 1) 2.0 (Note 2)	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	670 (Note 1) 490 (Note 2)	°C/W
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation T _A = 25°C Derate above 25°C	P _D	250 (Note 1) 385 (Note 2) 2.0 (Note 1) 3.0 (Note 2)	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	493 (Note 1) 325 (Note 2)	°C/W
Thermal Resistance, Junction-to-Lead	$R_{ heta JL}$	188 (Note 1) 208 (Note 2)	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

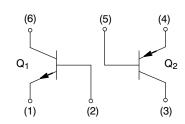
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 x 1.0 inch Pad



ON Semiconductor®

http://onsemi.com





SC-88 CASE 419B

MARKING DIAGRAM



3Z = Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device*	Package	Shipping [†]
UMZ1NT1G	SC-88 (Pb-Free)	3000 / Tape & Reel

*The "T1" suffix refers to a 7 inch reel.

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Q1: NPN ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

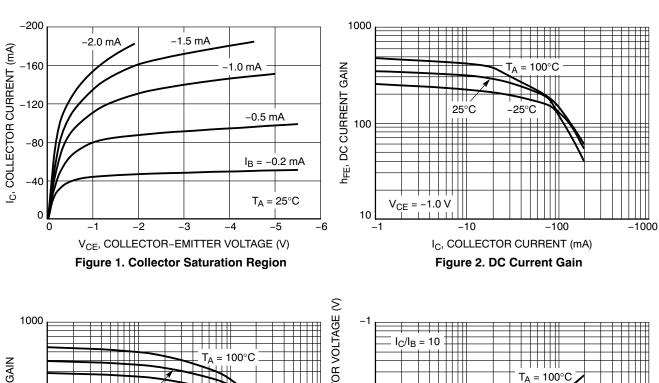
Characteristic	Symbol	Min	Тур	Max	Unit
Collector-Emitter Breakdown Voltage (I _C = 2.0 mAdc, I _B = 0)	V _(BR) CEO	50	-	-	Vdc
Collector–Base Breakdown Voltage (I _C = 10 μAdc, I _E = 0)	V _(BR) CBO	60	-	-	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)	V _{(BR)EBO}	7.0	-	-	Vdc
Collector–Base Cutoff Current (V _{CB} = 45 Vdc, I _E = 0)	І _{СВО}	-	-	0.1	μAdc
	lceo	- - -	- - -	0.1 2.0 1.0	μAdc μAdc mAdc
DC Current Gain (Note 3) (V _{CE} = 6.0 Vdc, I _C = 2.0 mAdc)	h _{FE}	200	-	400	-
Collector–Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 10 mAdc)	V _{CE(sat)}	_	-	0.25	Vdc
Transistor Frequency	f _⊤	-	114	-	MHz

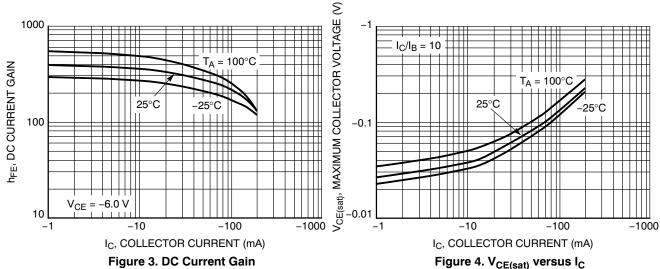
^{3.} Pulse Test: Pulse Width \leq 300 $\mu\text{s},\,\text{D.C.}\leq$ 2%.

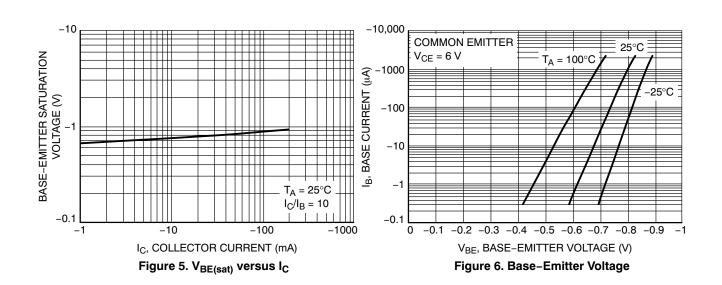
Q2: PNP ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
Collector-Emitter Breakdown Voltage (I _C = 2.0 mAdc, I _B = 0)	V	(BR)CEO	-50	-	-	Vdc
Collector–Base Breakdown Voltage ($I_C = 10 \mu Adc$, $I_E = 0$)	V	/ _{(BR)CBO}	-60	-	-	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)	V	/ _{(BR)EBO}	-7.0	-	-	Vdc
Collector-Base Cutoff Current (V _{CB} = 45 Vdc, I _E = 0)		I _{CBO}	_	-	-0.1	μAdc
		I _{CEO}	- - -	- - -	-0.1 -2.0 -1.0	μAdc μAdc mAdc
DC Current Gain (Note 3) (V _{CE} = 6.0 Vdc, I _C = 2.0 mAdc)		h _{FE}	200	-	400	-
Collector–Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 10 mAdc)	`	V _{CE(sat)}	-	-	-0.3	Vdc
Transistor Frequency		f _T	-	142	-	MHz

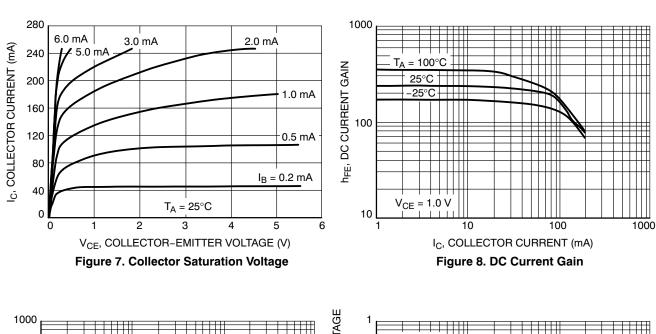
TYPICAL ELECTRICAL CHARACTERISTICS: PNP TRANSISTOR

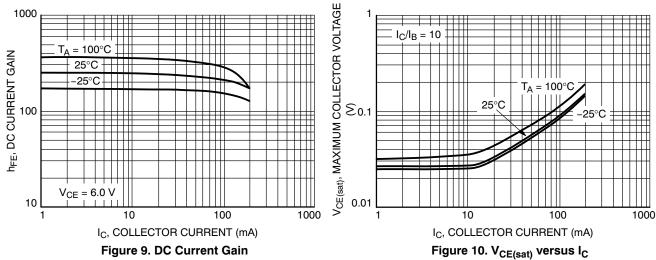


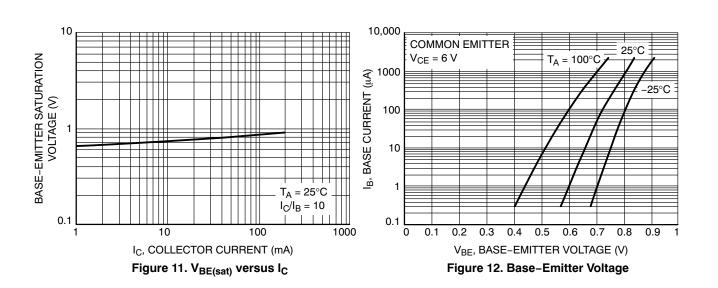




TYPICAL ELECTRICAL CHARACTERISTICS: NPN TRANSISTOR

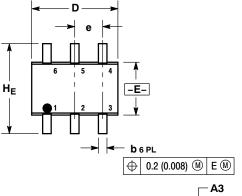






PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363 CASE 419B-02 ISSUE W





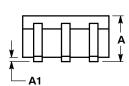
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
- 419B-01 OBSOLETE, NEW STANDARD 419B-02.

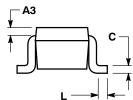
	MIL	LIMETE	ERS	INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
А3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
O	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
F	1 15	1 25	1.35	0.045	0.049	0.053

 e
 0.65 BSC
 0.026 BSC

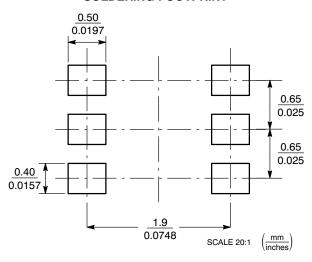
 L
 0.10
 0.20
 0.30
 0.004
 0.008
 0.012

 H_E
 2.00
 2.10
 2.20
 0.078
 0.082
 0.086





SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 📖 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA **Phone**: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative