



650V, 70A, V<sub>CE(on)</sub>= 1.9V Typical

## Ultra Fast NPT - IGBT®

The Ultra Fast 650V NPT-IGBT<sup>®</sup> family of products is the newest generation of IGBTs optimized for outstanding ruggedness and best trade-off between conduction and switching losses.

## **Features**

- Low Saturation Voltage
- Low Tail Current
- RoHS Compliant *M*

- Short Circuit Withstand Rated
- High Frequency Switching
- · Low Leakage Current



Combi (IGBT and Diode)

Unless stated otherwise, Microsemi discrete IGBTs contain a single IGBT die. This device is recommended for applications such as induction heating (IH), motor control, general purpose inverters and uninterruptible power supplies (UPS).

### MAXIMUM RATINGS

All Ratings:  $T_c = 25^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Ratings	Unit
V <sub>ces</sub>	Collector Emitter Voltage	650	v
V <sub>GE</sub>	Gate-Emitter Voltage	±30	v
I <sub>C1</sub>	Continuous Collector Current @ T <sub>c</sub> = 25°C	134	
I <sub>C2</sub>	Continuous Collector Current @ T <sub>c</sub> = 110°C	65	A
I <sub>CM</sub>	Pulsed Collector Current ①	260	
SCWT	Short Circuit Withstand Time: $V_{ce}$ = 600V, $V_{ge}$ = 15V, $T_c$ = 125°C	10	μs
P <sub>D</sub>	Total Power Dissipation @ $T_c = 25^{\circ}C$	595	W
T_,T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to 150	<b>0°</b>
TL	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	

## STATIC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage ( $V_{GE} = 0V$ , $I_{C} = 250uA$ )	650			
V <sub>GE(TH)</sub>	Gate Threshold Voltage ( $V_{CE} = V_{GE}$ , $I_{C} = 1.0$ mA, $T_{j} = 25$ °C)	3.5	5.0	6.5	) <b>(</b> - 14 -
V <sub>CE(ON)</sub>	Collector-Emitter On Voltage ( $V_{GE}$ = 15V, $I_{C}$ = 70A, $T_{j}$ = 25°C)		1.9	2.4	Volts
	Collector-Emitter On Voltage ( $V_{GE}$ = 15V, $I_{c}$ = 70A, $T_{j}$ = 125°C)		2.4		
	Collector-Emitter On Voltage ( $V_{GE}$ = 15V, $I_{c}$ = 140A, $T_{j}$ = 25°C)		2.6		
I <sub>ces</sub>	Collector Cut-off Current (V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V, T <sub>j</sub> = 25°C) <sup>(2)</sup>		40	850	μA
020	Collector Cut-off Current ( $V_{CE}$ = 650V, $V_{GE}$ = 0V, $T_j$ = 125°C) <sup>(2)</sup>		500		
I <sub>GES</sub>	Gate-Emitter Leakage Current (V <sub>GE</sub> = ±20V)			±250	nA

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

#### **DYNAMIC CHARACTERISTICS**

#### APT70GR65B2SCD30

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C <sub>ies</sub>	Input Capacitance	Capacitance		4250		
C <sub>oes</sub>	Output Capacitance	$V_{ge} = 0V, V_{ce} = 25V$		847		pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz		415		
V <sub>GEP</sub>	Gate to Emitter Plateau Voltage	Gate Charge		7.0		V
Q <sub>g</sub> ③	Total Gate Charge	V <sub>GE</sub> = 15V		226	305	
Q <sub>ge</sub>	Gate-Emitter Charge	V <sub>CE</sub> = 325V		26	35	nC
Q <sub>gc</sub>	Gate- Collector Charge	I <sub>с</sub> = 70А		104	140	
t <sub>d(on)</sub>	Turn-On Delay Time	Inductive Switching (25°C)	1	19		ns
t,	Current Rise Time	V <sub>cc</sub> = 433V		45		
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GE</sub> = 15V		170		
t <sub>r</sub>	Current Fall Time	I <sub>с</sub> = 70А		67		
E <sub>on2</sub> 5	Turn-On Switching Energy	$R_{g} = 4.3\Omega^{4}$		1790	2685	1
E <sub>off</sub>	Turn-Off Switching Energy	T <sub>J</sub> = +25°C		1460	1970	μJ
t <sub>d(on)</sub>	Turn-On Delay Time	Inductive Switching (125°C)	1	19	Ì	
t,	Current Rise Time	V <sub>cc</sub> = 433V		45	Î	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GE</sub> = 15V		190		
t <sub>r</sub>	Current Fall Time	I <sub>с</sub> = 70А		74		
E <sub>on2</sub> 5	Turn-On Switching Energy	$R_{g} = 4.3\Omega^{(4)}$		1760	2640	1
E <sub>off</sub>	Turn-Off Switching Energy	T <sub>J</sub> = +125°C		1720	2580	μJ

#### THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	Min	Тур	Max	Unit
R <sub>ejc</sub>	Junction to Case Thermal Resistance			.21	°C/W
R <sub>eja</sub>	Junction to Ambient Thermal Resistance			40	
W <sub>T</sub>	Deskage Weight		.22		οz
	Package Weight		6.2		g

1 Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.

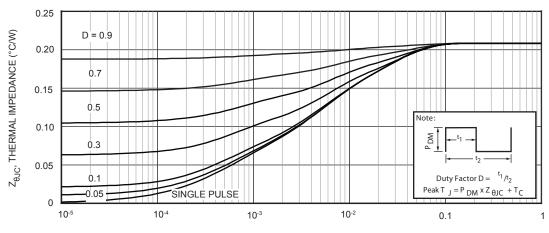
2 Pulse test: Pulse Width < 380µs, duty cycle < 2%.

3 See Mil-Std-750 Method 3471.

4 R<sub>G</sub> is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)

5  $E_{on2}$  is the energy loss at turn-on and includes the charge stored in the freewheeling diode.

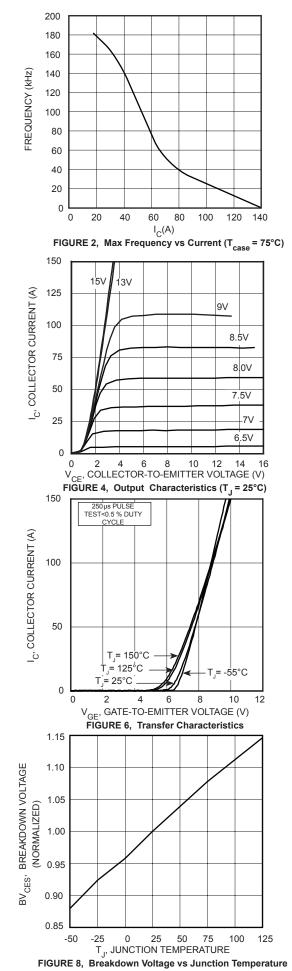
 $_{\text{onz}}^{\text{onz}}$  = 0.5 standard JESD24-1. Microsemi reserves the right to change, without notice, the specifications and information contained herein.

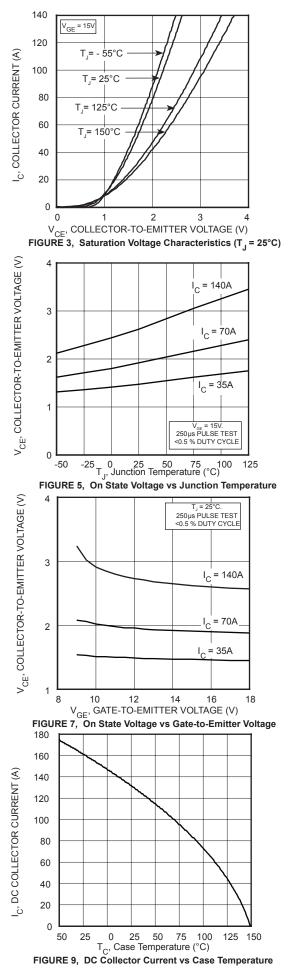


RECTANGULAR PULSE DURATION (SECONDS) Figure 1, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

#### **TYPICAL PERFORMANCE CURVES**

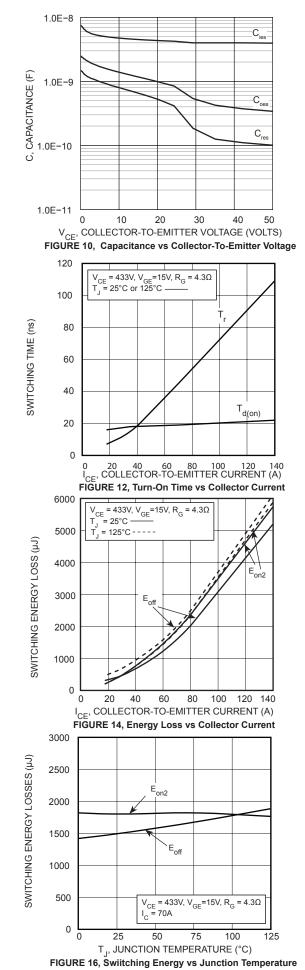
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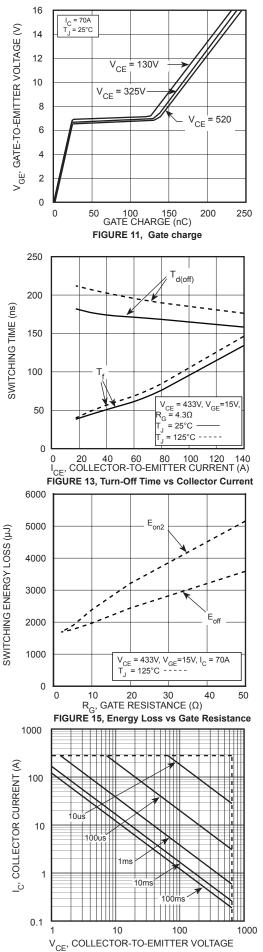




#### **TYPICAL PERFORMANCE CURVES**

#### APT70GR65B2SCD30





# ZERO RECOVERY LOW LEAKAGE SIC ANTI-PARALLEL DIODE

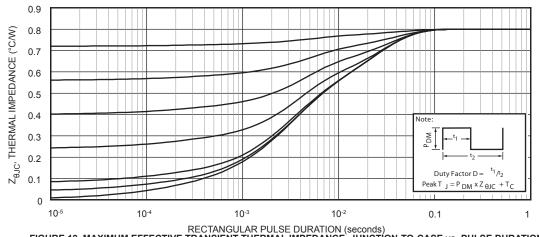
#### **MAXIMUM RATINGS**

All Ratings:  $T_{C} = 25^{\circ}C$  unless otherwise specified.

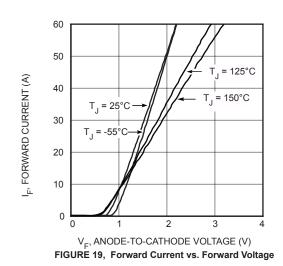
Symbol	Characteristic / Test Conditions		Ratings	Unit
	Maximum D.C. Forward Current	T <sub>c</sub> = 25°C	46	
I <sub>F</sub>	Maximum D.C. Forward Current	T <sub>c</sub> = 85°C	30	Amps
I <sub>FSM</sub>	Non-Repetitive Forward Surge Current ( $T_J = 25^{\circ}C$ , $t_p = 10$ ms, Half Sine)		247	

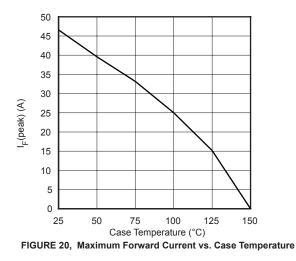
#### STATIC ELECTRICAL CHARACTERISTICS

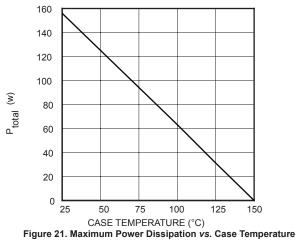
Symbol	Characteristic / Test Conditions		Min	Тур	Мах	Unit
V <sub>F</sub>	Forward Voltage	$I_{F} = 30A T_{J} = 25^{\circ}C$		1.5		Volts
		I <sub>F</sub> = 30A, T <sub>J</sub> = 150°C		1.9		
Q <sub>c</sub>	Total Capactive Charge V <sub>R</sub> = 325V, I <sub>F</sub> = 30A, di/dt = -500A/ $\mu$ s, T <sub>J</sub> = 25°C			150		nC
	Junction Capacitance $V_{R}$ = 1V, $T_{J}$ = 25°C, f = 1MHz			945		
C <sub>T</sub>	Junction Capacitance $V_R = 200V$ , $T_J = 25^{\circ}C$ , f = 1MHz			138		pF
	Junction Capacitance $V_{R}$ = 400V, $T_{J}$ = 25°C, f = 1MHz			105		

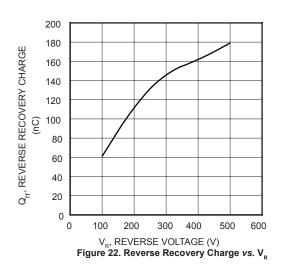


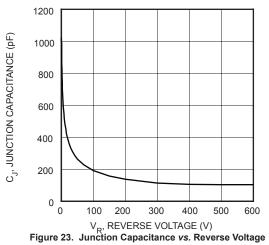




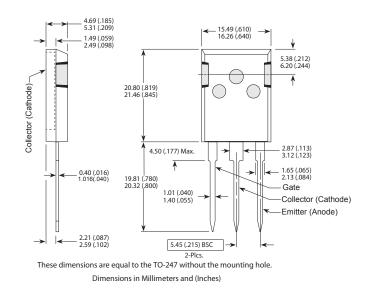








T-MAX<sup>®</sup> (B2) Package Outline



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