

# Ultrafast Rectifier, 3 A FRED Pt®

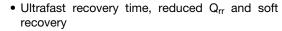


**SMA (DO-214AC)** 

# Cathode Anode

PRODUCT SUMMARY					
Package	SMA (DO-214AC)				
I <sub>F(AV)</sub>	3 A				
$V_{R}$	600 V				
V <sub>F</sub> at I <sub>F</sub>	1.35 V				
t <sub>rr</sub> typ.	41 ns				
T <sub>J</sub> max.	175 °C				
Diode variation	Single die				

#### **FEATURES**





RoHS

COMPLIANT HALOGEN

FREE

- 175 °C maximum operating junction temperature
- For PFC CRM/CCM, snubber operation
- Total Collin Colli, chapper
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION/APPLICATIONS**

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	$V_{RRM}$		600	V		
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>L</sub> = 103 °C <sup>(1)</sup>	3	V		
Non-repetitive peak surge current per leg	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	55	Α		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C		

#### Note

(1) Mounted on PCB with minimum pad size

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	.,
Forward voltage	$V_{F}$	I <sub>F</sub> = 3 A	-	1.15	1.35	V
	٧F	$I_F = 3 \text{ A}, T_J = 150 ^{\circ}\text{C}$	-	0.99	1.2	
Reverse leakage current	current I <sub>R</sub>	$V_R = V_R$ rated	-	-	3	
		$T_J = 150  ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	-	100	μΑ
Junction capacitance	C <sub>T</sub>	$V_{R} = 600 \text{ V}$	-	3.9	-	pF





<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	41	-	ns
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	52	-	
Reverse recovery time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	65	
		T <sub>J</sub> = 25 °C		-	38	-	ı
		T <sub>J</sub> = 125 °C		-	52	-	
Peak recovery current	,	T <sub>J</sub> = 25 °C	$I_F = 3 A$	-	5.6	-	А
	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 390 \text{ V}$	-	7.3	-	_ ^
Reverse recovery charge	0	T <sub>J</sub> = 25 °C	11	-	108	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	193	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55	-	175	°C
Thermal resistance, junction to case	R <sub>thJC</sub> (1)		-	-	20	°C/W
Thermal resistance, junction to ambient	R <sub>thJA</sub> <sup>(1)</sup>		-	-	95	C/VV
Approximate Weight				0.07		g
Approximate Weight				0.002		oz.
Marking device		Case style SMA (DO-214AC)		31	J6	

#### Note

<sup>(1)</sup> Mounted on PCB with minimum pad size

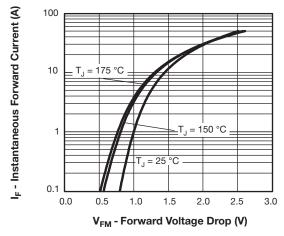


Fig. 1 - Typical Forward Voltage Drop Characteristics

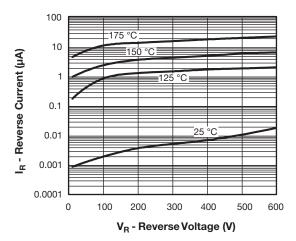


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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## Vishay Semiconductors

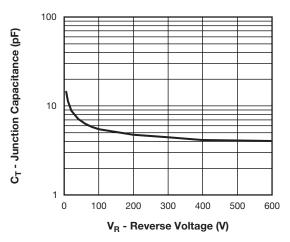


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

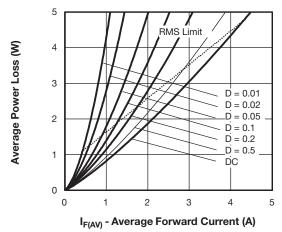


Fig. 5 - Forward Power Loss Characteristics

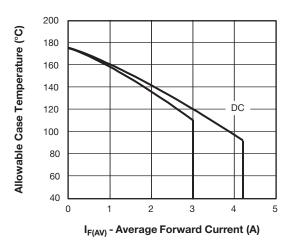


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

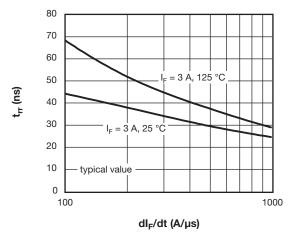


Fig. 6 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

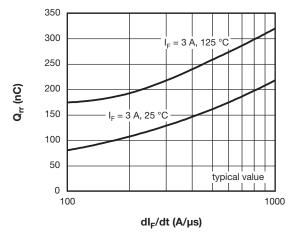
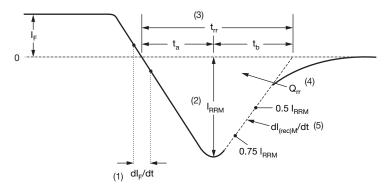


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt





- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (4)  $\rm Q_{rr}$  area under curve defined by  $\rm t_{rr}$  and  $\rm I_{RRM}$

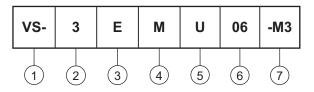
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 8 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

#### **Device code**



- Vishay Semiconductors product
- Current rating (3 = 3 A)
- 3 Circuit configuration:

E = Single diode

- 4 M = SMA package
- 5 Process type,

U = Ultrafast recovery

- 6 Voltage code (06 = 600 V)
- 7 M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-3EMU06-M3/5AT	5AT	7500	13"diameter plastic tape and reel			

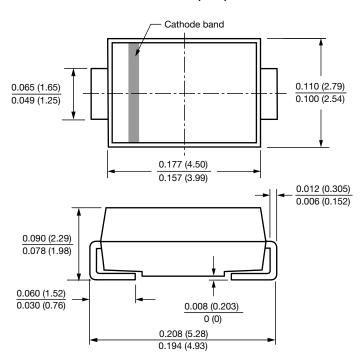
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95400				
Part marking information	www.vishay.com/doc?95472				
Packaging information	www.vishay.com/doc?95404				



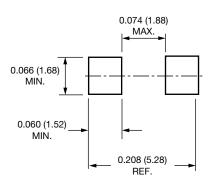
## **SMA**

#### **DIMENSIONS** in inches (millimeters)

#### **DO-214AC (SMA)**



#### Mounting Pad Layout





## **Legal Disclaimer Notice**

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