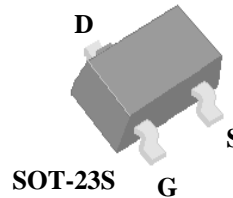




- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device
- ▼ RoHS Compliant & Halogen-Free

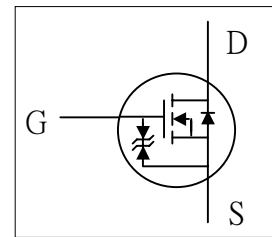


BV_{DSS}	60V
$R_{DS(ON)}$	2Ω
I_D^3	450mA

Description

AP6N2K0E series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The SOT-23S package is widely preferred for commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings @ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A=25^\circ\text{C}$	Drain Current ³ , V_{GS} @ 10V	450	mA
$I_D @ T_A=70^\circ\text{C}$	Drain Current ³ , V_{GS} @ 10V	360	mA
I_{DM}	Pulsed Drain Current ¹	950	mA
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation ³	0.7	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³	180	$^\circ\text{C}/\text{W}$



AP6N2K0EN

Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=450mA$	-	-	2	Ω
		$V_{GS}=4.5V, I_D=200mA$	-	-	4	Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1	-	3	V
g_{fs}	Forward Transconductance	$V_{DS}=10V, I_D=450mA$	-	0.85	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=48V, V_{GS}=0V$	-	-	25	μA
I_{GSS}	Gate-Source Leakage	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 30	μA
Q_g	Total Gate Charge	$I_D=450mA$	-	1.8	2.9	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=30V$	-	0.4	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=10V$	-	0.3	-	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=30V$	-	6	-	ns
t_r	Rise Time	$I_D=450mA$	-	6	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega$	-	12	-	ns
t_f	Fall Time	$V_{GS}=10V$	-	6	-	ns
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	42	67.2	pF
C_{oss}	Output Capacitance	$V_{DS}=25V$	-	11	-	pF
C_{riss}	Reverse Transfer Capacitance	$f=1.0MHz$	-	7	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_S=450mA, V_{GS}=0V$	-	-	1.3	V

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board, $t \leq 10sec$; 400°C/W when mounted on min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

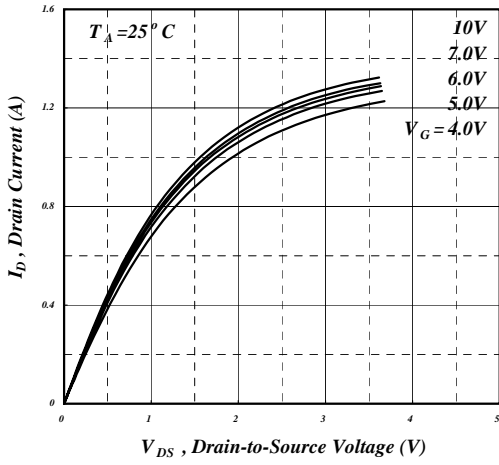


Fig 1. Typical Output Characteristics

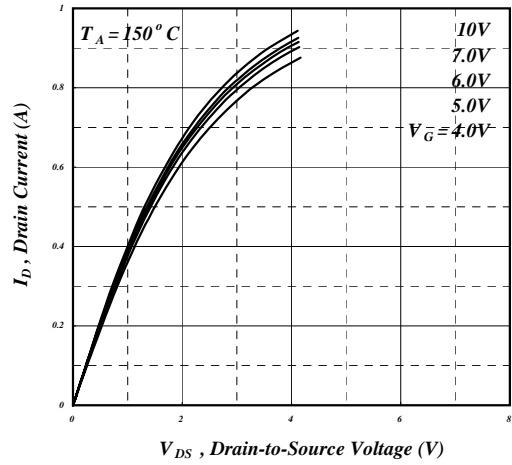


Fig 2. Typical Output Characteristics

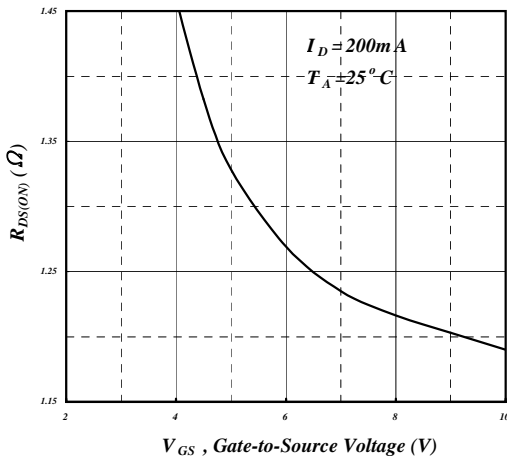


Fig 3. On-Resistance v.s. Gate Voltage

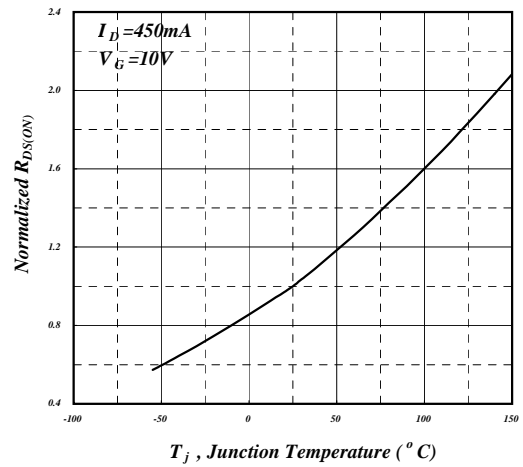


Fig 4. Normalized On-Resistance v.s. Junction Temperature

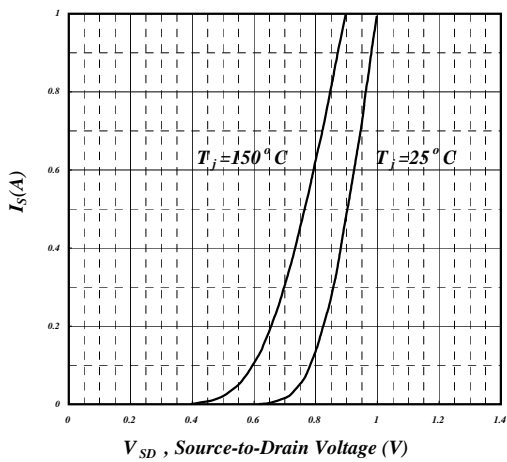


Fig 5. Forward Characteristic of Reverse Diode

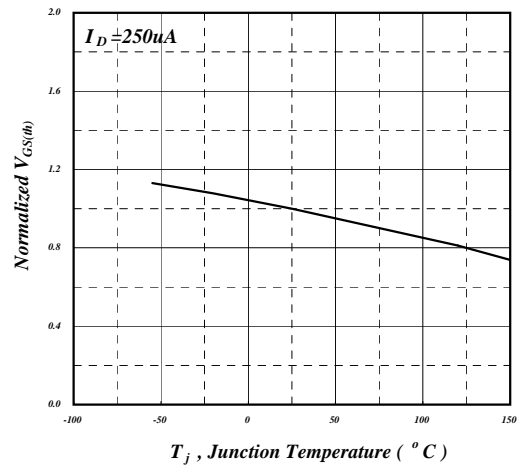


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

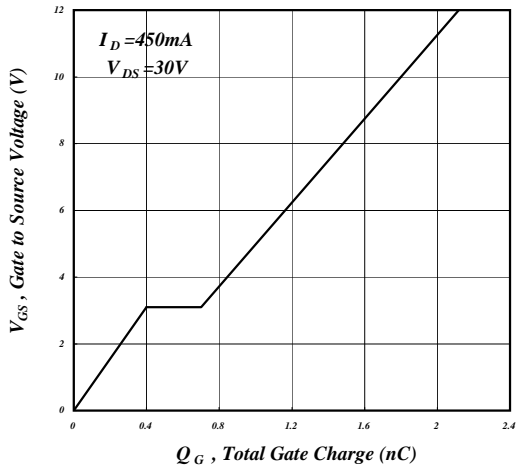


Fig 7. Gate Charge Characteristics

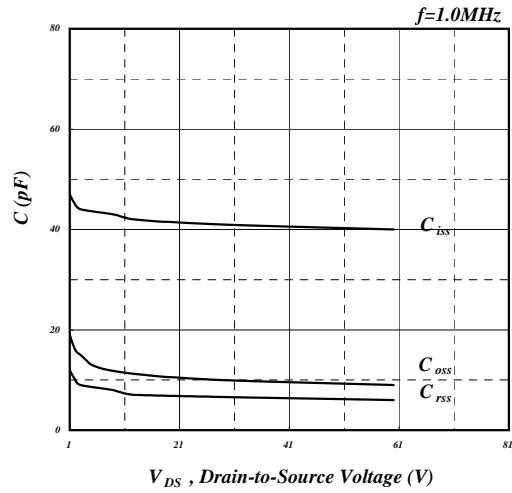


Fig 8. Typical Capacitance Characteristics

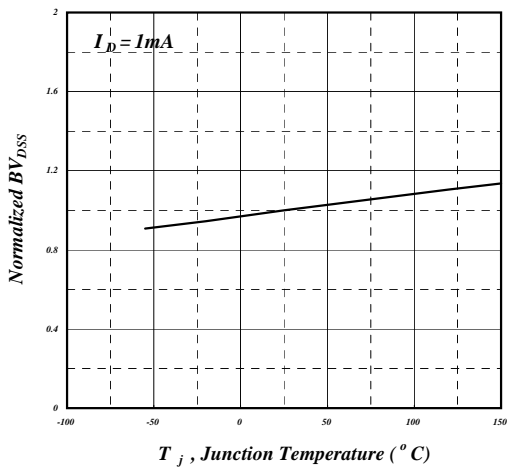


Fig 9. Normalized BVDSS v.s. Junction Temperature

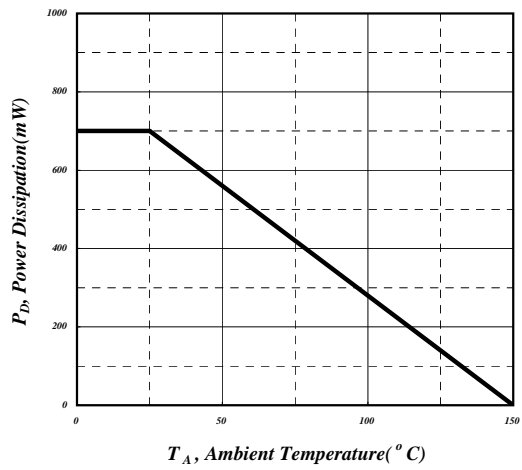


Fig 10. Total Power Dissipation

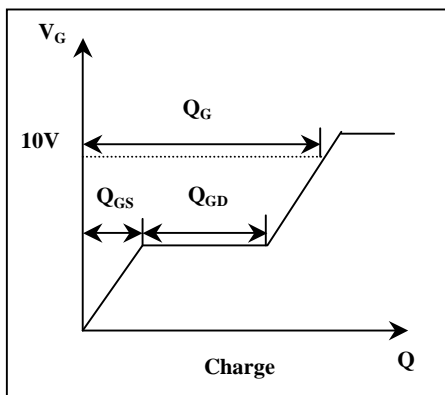


Fig 11. Gate Charge Waveform

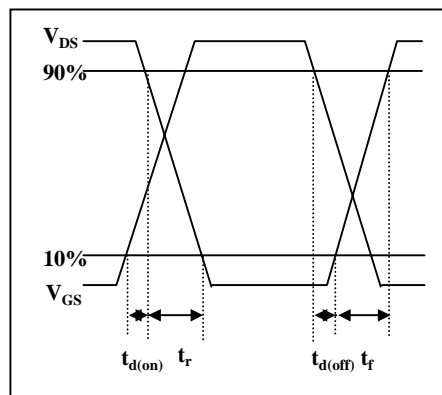


Fig 12. Switching Time Waveform



MARKING INFORMATION

