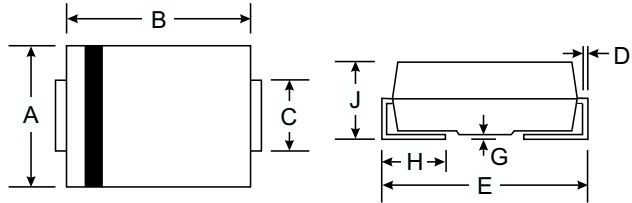


Features

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop, High Efficiency
- Surge Overload Rating to 1 0 0A Peak
- Low Power Loss
- Super-Fast Recovery Time
- Plastic Case Material has UL Flammability Classification Rating 94V-O



Mechanical Data

- Case: SMB/DO-214AA,DO-214AB, Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: SMC Weight: 0.20 grams (approx.)
- **Lead Free: For RoHS / Lead Free Version**

SMC/DO-214AB		
Dim	Min	Max
A	5.59	6.22
B	6.60	7.11
C	2.75	3.25
D	0.152	0.305
E	7.75	8.13
F	2.00	2.62
G	0.051	0.203
H	0.76	1.27
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Characteristic	Symbol	ES4A	ES4B	ES4C	ES4D	ES4E	ES4G	ES4J	Unit
Peak Repetitive Reverse Voltage	V _{RRM}								
Working Peak Reverse Voltage	V _{RWM}	50	100	150	200	300	400	600	V
DC Blocking Voltage	V _R								
RMS Reverse Voltage	V _{R(RMS)}	35	70	105	140	210	280	420	V
Average Rectified Output Current @T _L = 120°C	I _O	4.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	100							A
Forward Voltage @I _F = 4.0A	V _{FM}	0.95				1.30		1.7	V
Peak Reverse Current @T _A = 25°C	I _{RM}					5.0			μA
At Rated DC Blocking Voltage @T _A = 100°C						500			
Reverse Recovery Time (Note 1)	t _{rr}					35			nS
Typical Junction Capacitance (Note 2)	C _j					45			pF
Typical Thermal Resistance (Note 3)	R _{θJL}					35			°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +150							°C

Note: 1. Measured with I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A. See figure 5.
 2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.
 3. Mounted on P.C. Board with 8.0mm² land area.

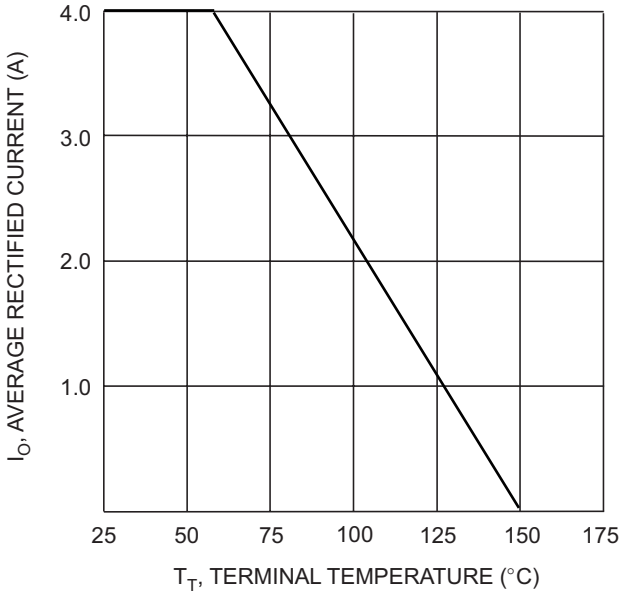


Fig. 1 Forward Current Derating Curve

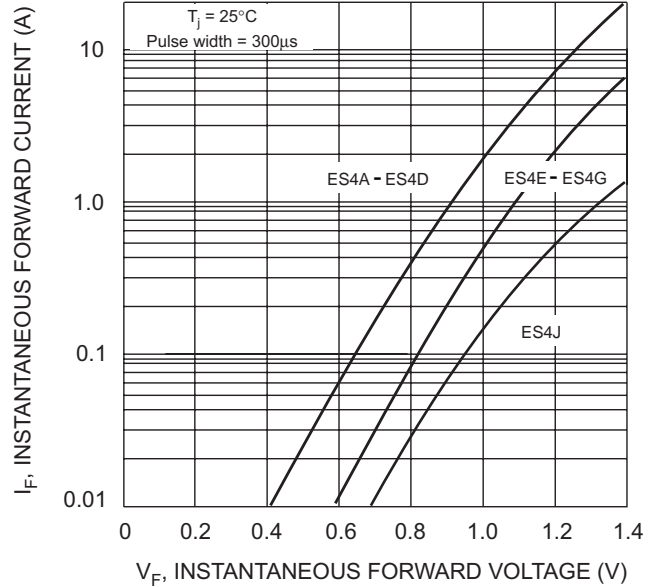


Fig. 2 Typical Forward Characteristics

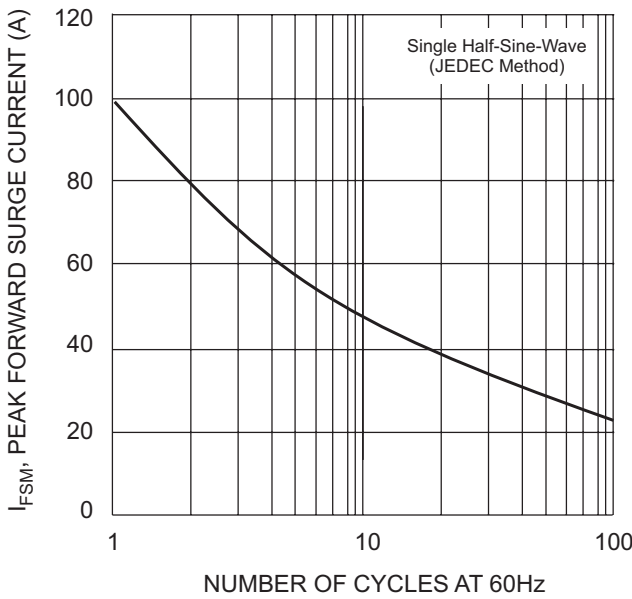


Fig. 3 Surge Current Derating Curve

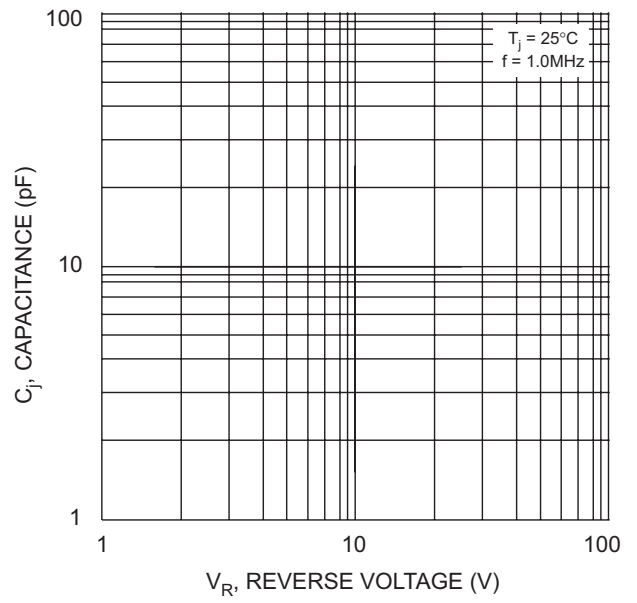
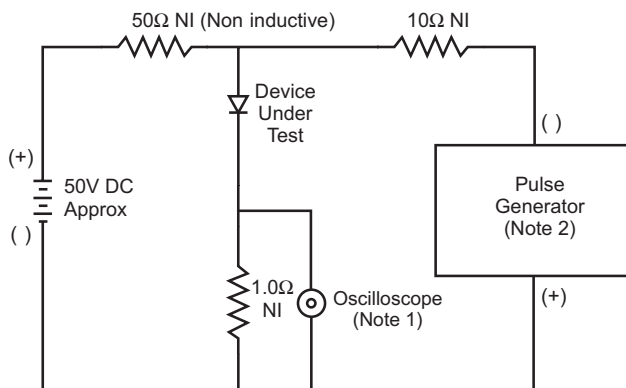


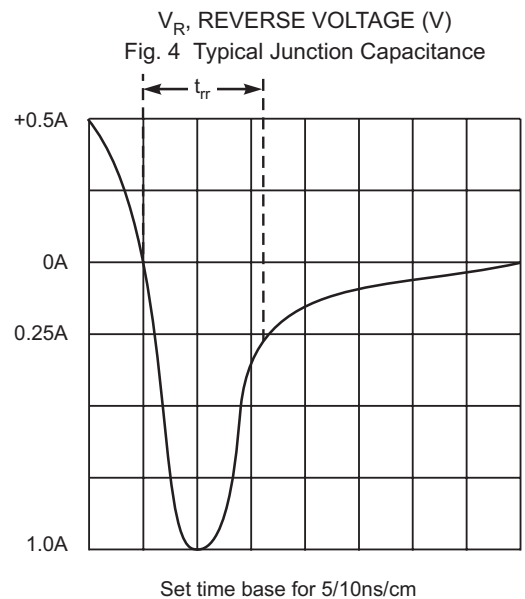
Fig. 4 Typical Junction Capacitance



Notes:

1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
2. Rise Time = 10ns max. Input Impedance = 50Ω.

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit



Set time base for 5/10ns/cm