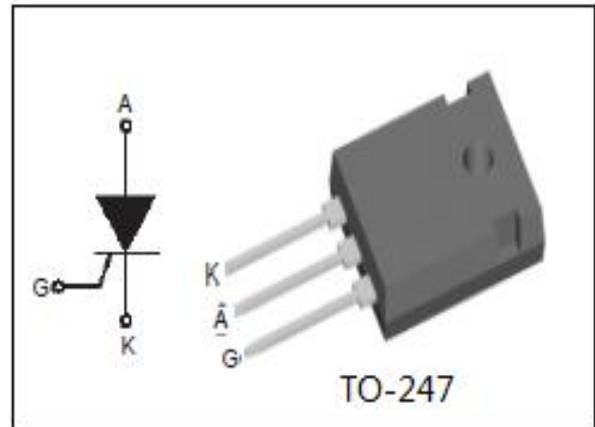


isc Thyristors
40TPS12A
DESCRIPTION

- With TO-247 packaging
- Long-term stability
- Thyristor for line frequency
- Planar passivated chip
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Switching applications
- Line rectifying 50/60 Hz


ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	1200	V
V _{RRM}	Repetitive peak reverse voltage	1200	V
I _{T(AV)}	Average forward current @T _c =79°C	35	A
I _{T(RMS)}	RMS on-state current	55	A
I _{TSM}	Surge non-repetitive on-state current (1/2 cycle,sine wave)	50HZ 60HZ	500 600
P _{G(AV)}	Average gate power dissipation	2.5	W
T _j	Operating junction temperature	-40~125	°C
T _{stg}	Storage temperature	-40~150	°C

ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RM} =V _{RRM} V _{DM} =V _{DRM}		0.5 10	mA
I _{DRM}	Repetitive peak off-state current				
V _{TM}	On-state voltage	I _{TM} = 110A		1.85	V
I _{GT}	Gate-trigger current	V _D = 6V		150	mA
V _{GT}	Gate-trigger voltage	V _D = 6V		2.5	V
R _{th(j-c)}	Thermal resistance	Junction to case		0.6	°C/W