

SMD LVC MOS output
11.4 x 9.6 x 2.5 mm



RoHS Compliance

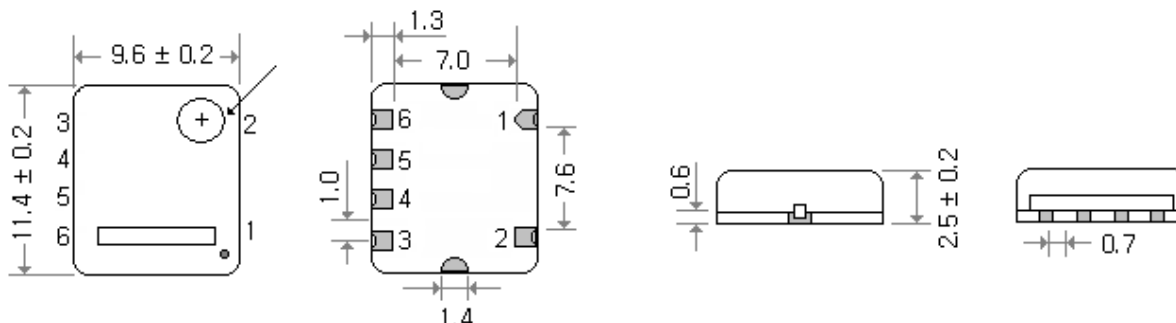
Features

- Wide frequency range : [200.01 MHz ~ 800.0 MHz]
- Custom frequencies can easily be configured .

General Specifications

Parameters		Electrical Spec.						
Input Voltage (V _{DD})		3.3 V ± 5 %						
Frequency Range		200.0 MHz ~ 800.0 MHz						
Output Wave Form		LVC MOS output						
Initial Calibration Tolerance		Models without mechanical trimmer : < ± 2.0 ppm at +25°C ±2°C.						
Frequency Stability (ppm)		± 0.5 ppm	± 1.0 ppm	± 1.5 ppm	± 2.0 ppm	± 2.5 ppm	± 3.0 ppm	± 5.0 ppm
Frequency Stability vs Temperature	0°C to 50°C	○	○	○	○	○	○	○
	-10°C to 60°C	△	○	○	○	○	○	○
	-20°C to 70°C	X	○	○	○	○	○	○
	-30°C to 75°C	X	○	○	○	○	○	○
	-40°C to 85°C	X	○	○	○	○	○	○
○ : available △ : please contact us X : not available								
Frequency Stability	vs Aging	± 1.0 ppm / year max. at 25C						
	vs Voltage Change	± 0.3 ppm max. , for a ±5% input voltage change .						
	vs Load Change	± 0.3 ppm max. , for a ±10% load condition change .						
	vs Reflow (SMD type)	± 1.0 ppm max. , 1 reflow and measured 24 hours afterwards .						
Current Consumption (max.)		40.0 mA (max.) [frequency dependent]						
Output Logic Levels	Logic High " 1 "	90% of V _{DD} min.						
	Logic Low " 0 "	10% of V _{DD} max.						
Output Load		15 pF						
Start - Up Time (T _s)		5.0 m sec. (typ.) , 10.0 m sec. (max.)						
Output Format		DC block , AC coupled						
Rise Time (T _r) / Fall Time (T _f)		1.2 n sec. (typical) [0.3 V ↔ 3.0 V , 15 pF load]						
Duty Cycle		Standard: 50 % ± 10 % ; Option: 50 % ± 5 % ; Measured at 50% V _{DD} .						
Storage Temperature		- 50°C to 100°C						
Aging		± 3 ppm per year (max.)						
Phase Noise (typical) [622.080 MHz]		Offset	10 Hz	100 Hz	1K Hz	10 KHz	100KHz	
		dBc / Hz	-50	-78	-102	-115	-108	

General Specifications (Unit : mm)



Pad Connections :
 Pad 1 , 2 , 4 : Ground
 Pad 3 : Output
 Pad 5 : Voltage control for VCTCXO ; No connection for TCXO
 Pad 6 : Supply Voltage

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